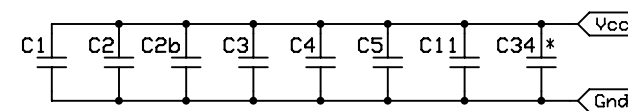


GLUE LOGIC EQUATIONS ON THIS PAGE

BNK0 = !(A18 : A17 : A16)

VADR = VDA : VPA

Symbols: & = logical AND
: = logical OR
! = logical NOT



Decoupling Capacitors -- 0.1 uf @ 50v

*C34 → Y1

MPU INTERFACE

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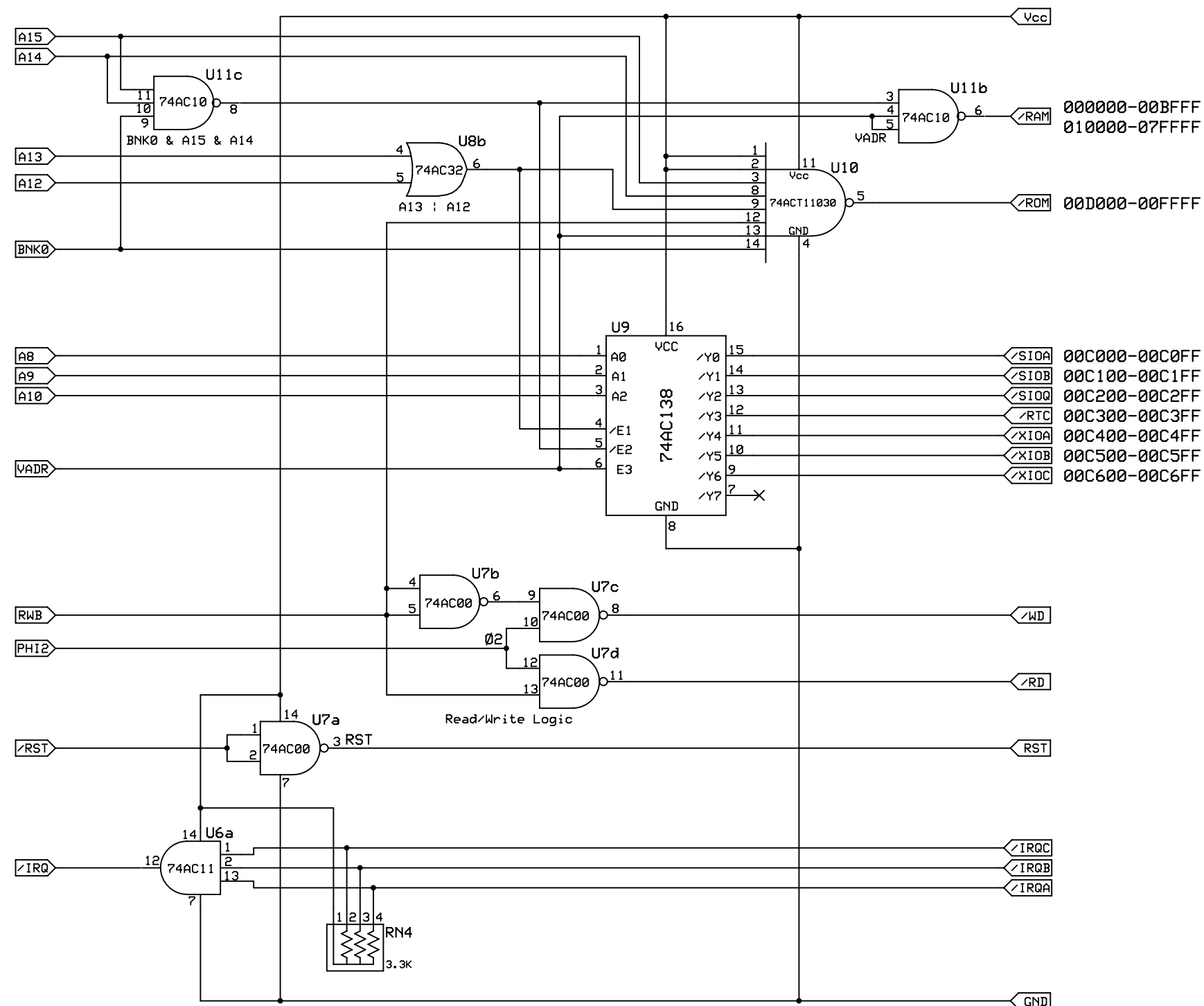
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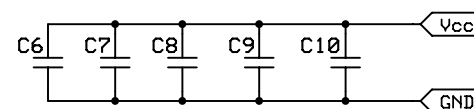
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GLUE LOGIC EQUATIONS ON THIS PAGE

!RAM = VADDR & (!BNK0 ! (A15 & A14))
 !ROM = VADDR & BNK0 & RWB & A15 & A14 & (A13 ! A12)
 IO = VADDR & BNK0 & A15 & A14 & !(A12 ! A13)
 !IRQ = !(IRQA & IRQB & IRQC)

Symbols: & = logical AND
 ! = logical OR
 ! = logical NOT



Decoupling Capacitors -- 0.1 uf @ 50v

GLUE LOGIC

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