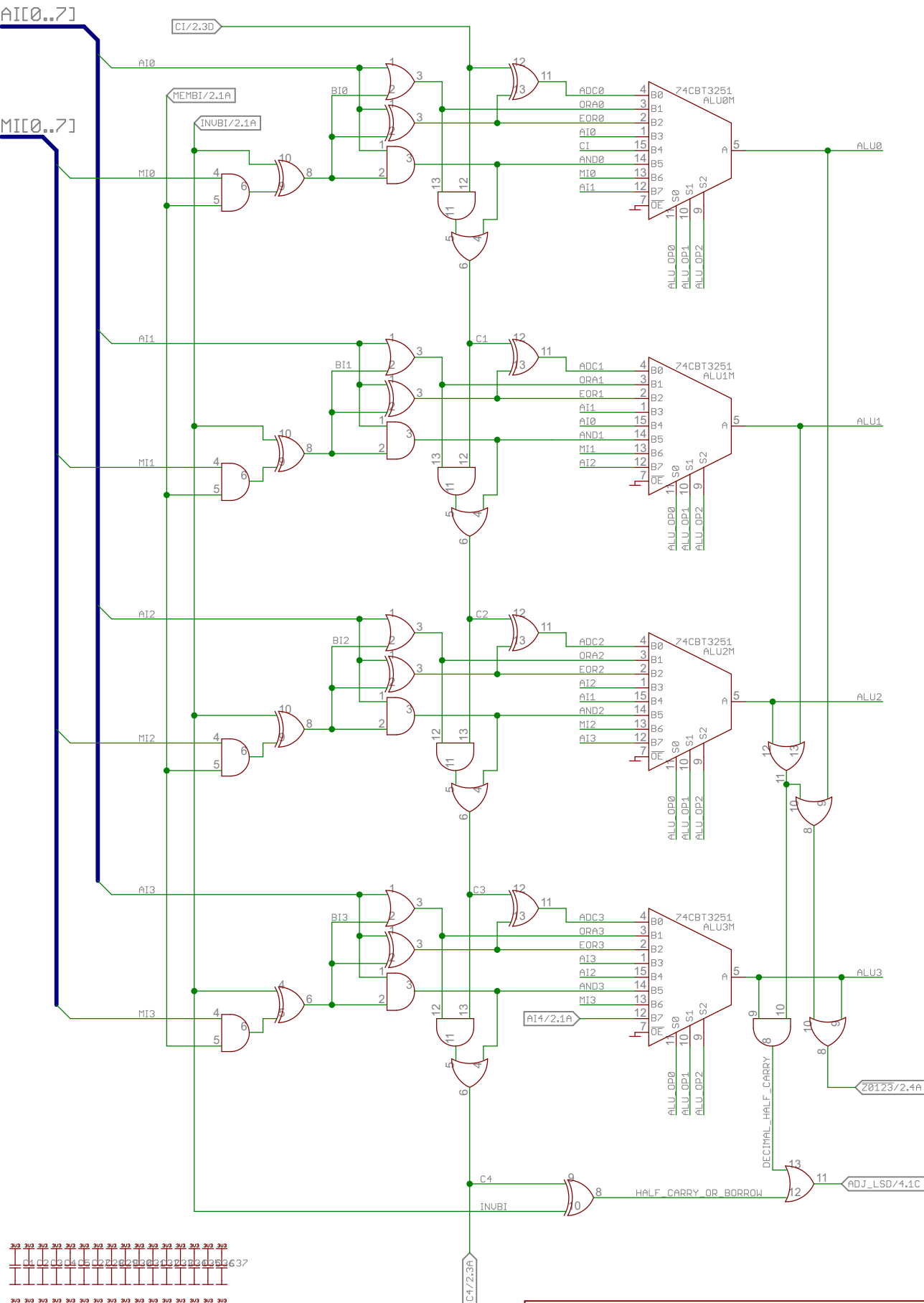


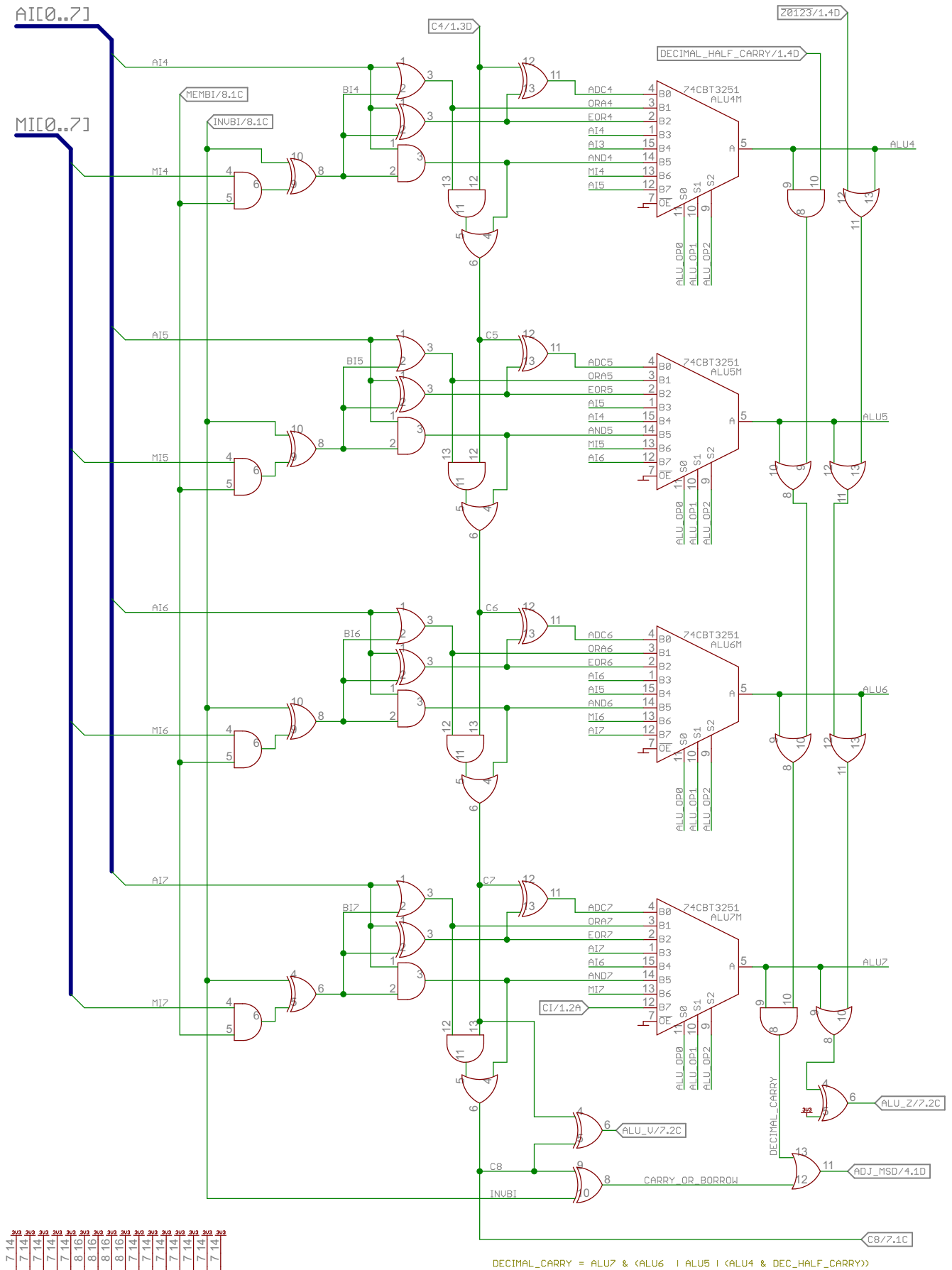
ALU - LOWER NIBBLE



The diagram illustrates a 16-bit bus system. At the top, a horizontal bus line is labeled with 16 input devices: 3/1, 3/2, 3/3, 3/4, 3/5, 3/6, 3/7, 3/8, 3/9, 3/10, 3/11, 3/12, 3/13, 3/14, 3/15, and 3/16. Each device is connected to the bus via a vertical line. Below the bus, there are 16 output devices, each labeled with a number from 1 to 16. Each output device is connected to the bus via a vertical line. The output devices are arranged in two columns of eight. The first column contains output devices 1 through 8, and the second column contains output devices 9 through 16. The output devices are labeled with their respective numbers: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, and 16. The output devices are arranged in two columns of eight. The first column contains output devices 1 through 8, and the second column contains output devices 9 through 16. The output devices are labeled with their respective numbers: 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, and 16.

Project: TTL-6502			
Date:	Rev:	Last: not saved!	
Name: ttl		Drawn: A0	Sheet: 1/9

ALU - UPPER NIBBLE



```
DECIMAL_CARRY = ALU7 & (ALU6 | ALU5 | (ALU4 & DEC_HALF_CARRY))
```