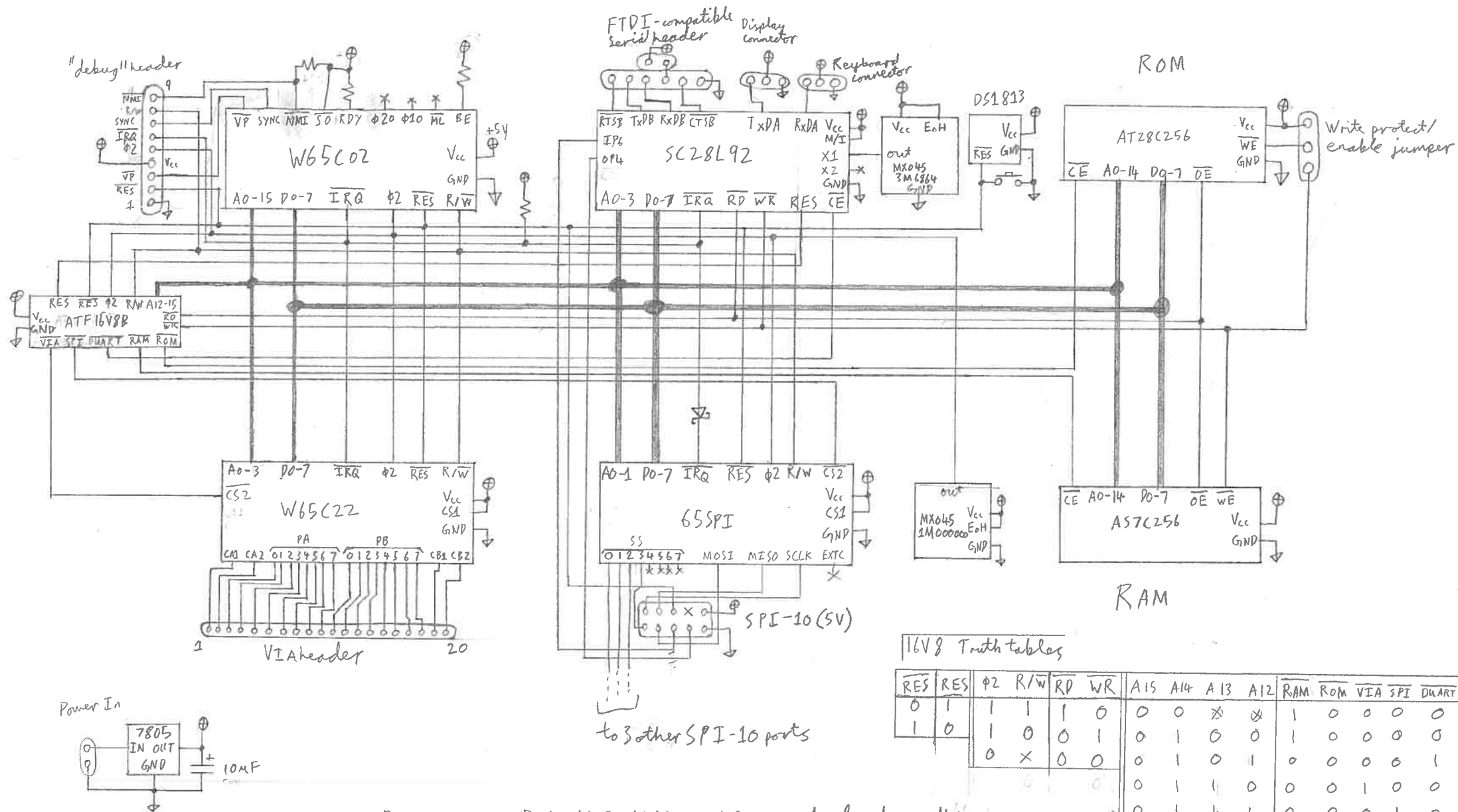


Ithiara V3.1



16V8 Truth tables

RES	RES	φ2	R/W	RD	WR	A15	A14	A13	A12	RAM	ROM	VIA	SPI	DUART
0	1	1	1	1	0	0	0	x	x	1	0	0	0	0
1	0	1	0	0	1	0	1	0	0	1	0	0	0	0
		0	x	0	0	0	1	0	1	0	0	0	0	1
						0	1	1	0	0	0	1	0	0
						0	1	1	1	0	0	0	1	0
						1	x	x	x	0	1	0	0	0

These are truth values, not necessarily signal levels.

Bypass caps on ROM, IC02, 16V8, 28L92, and clock oscillators are not shown

There are no bypass caps on the RAM, the VIA, or the 65SPI, because it was too awkward to get them in.

This does not reflect the physical layout.

Power is supplied from a 3 cell Life battery, going through a switched lead.

Pins not marked are not connected.