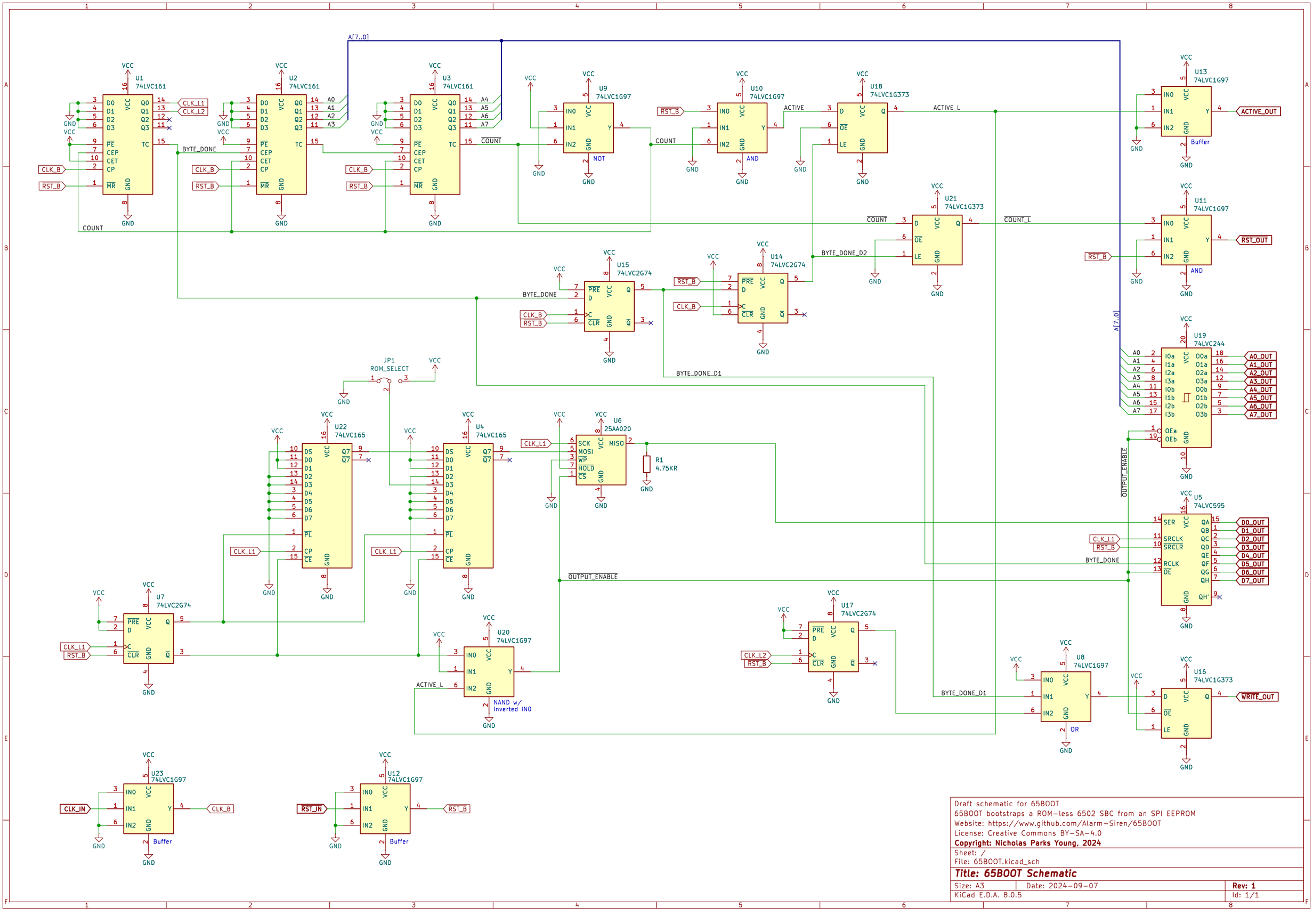

Draft schematic for 65BOOT
65BOOT bootstraps a ROM-less 6502 SBC from an SPI EEPROM
Website: https://www.github.com/Alarm-Siren/65BOOT
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File: 65BOOT.kicad_sch

Title: 65BOOT Schematic

Size: A3	Date: 2024-09-07	**Rev: 1**
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