

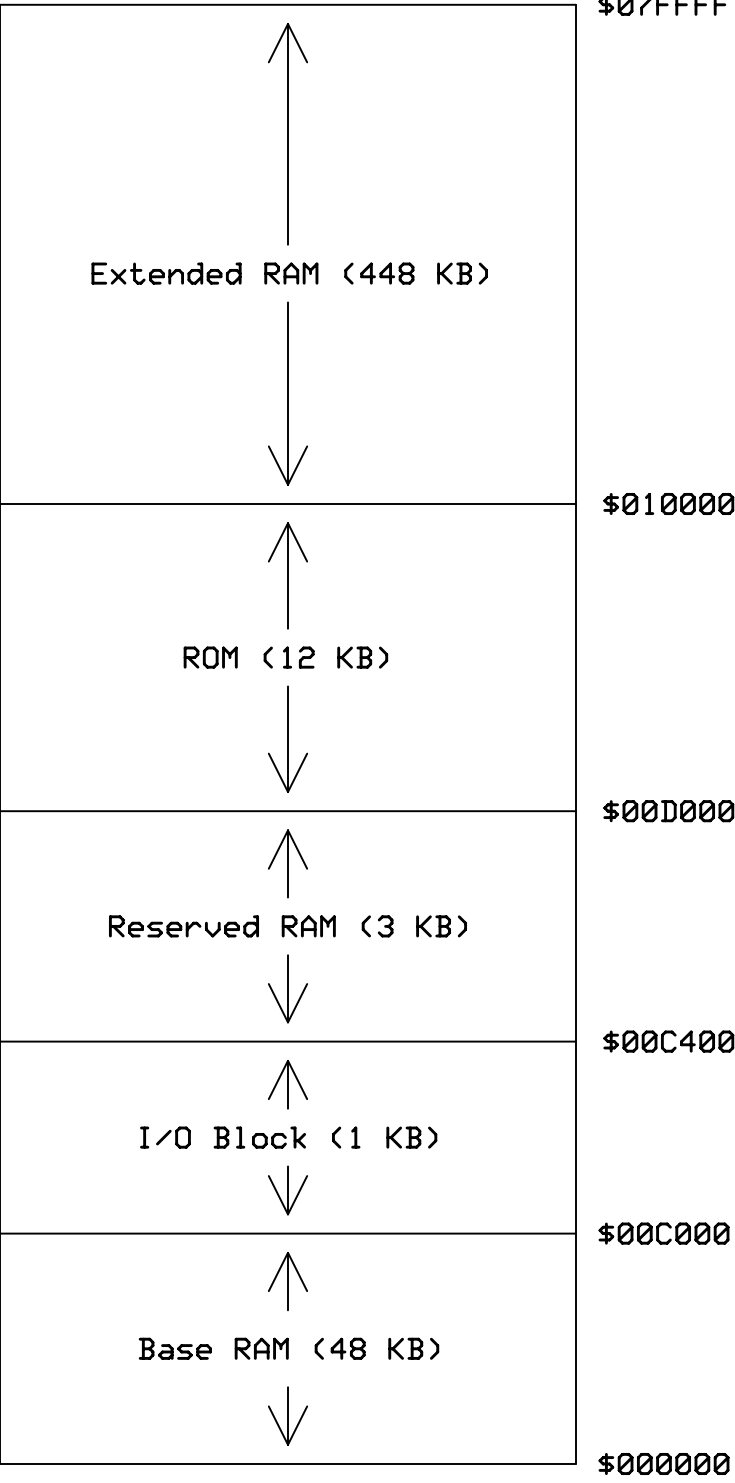
PROOF OF CONCEPT V1

SINGLE-BOARD COMPUTER

512 Kilobyte Static RAM System

Powered by the W65C816S Microprocessor

Designed by BigDumbDinosaur



ARCHITECTURE

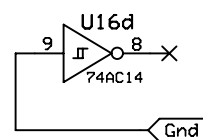
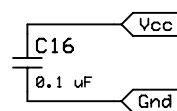
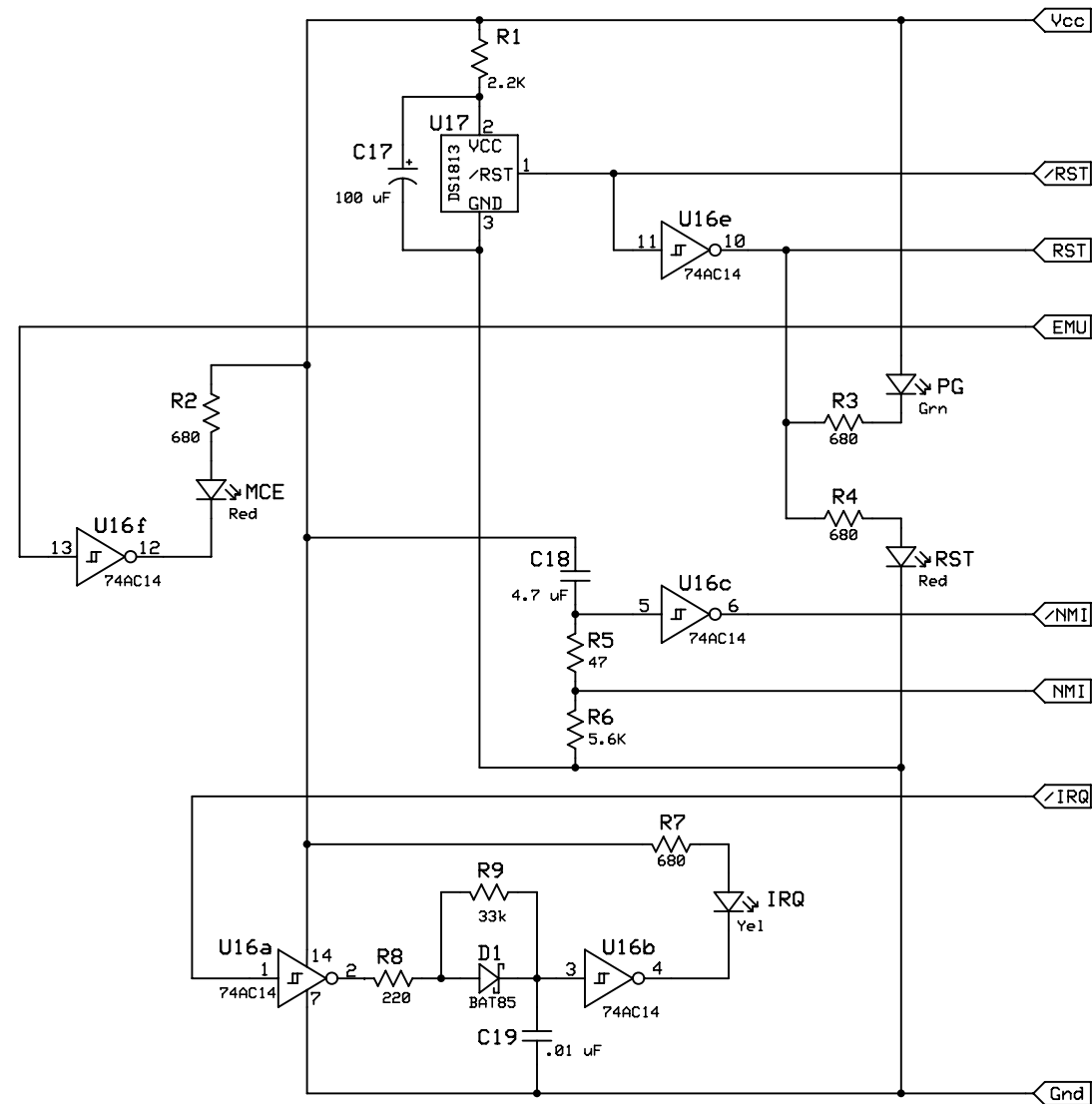
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W65C816S SINGLE BOARD COMPUTER		
BigDumbDinosaur	2024/01/01	Page 1 of 10
	Rev 1.5	

Undecoded	\$00C3FF
Expansion Select C	\$00C380
Expansion Select B ¹	\$00C300
Expansion Select A ¹	\$00C280
Real-Time Clock	\$00C200
υQUART Channel IRQ Status ¹	\$00C180
υQUART Channels C-D, timer B	\$00C100
υQUART Channels A-B, timer A	\$00C080
	\$00C000

¹Not wait-stated.

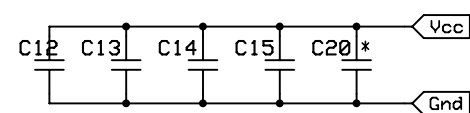
INPUT/OUTPUT MAP

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BigDumbDinosaur	2024/01/01	Page 2 of 10
	Rev 1.5	

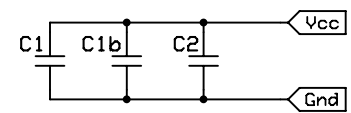
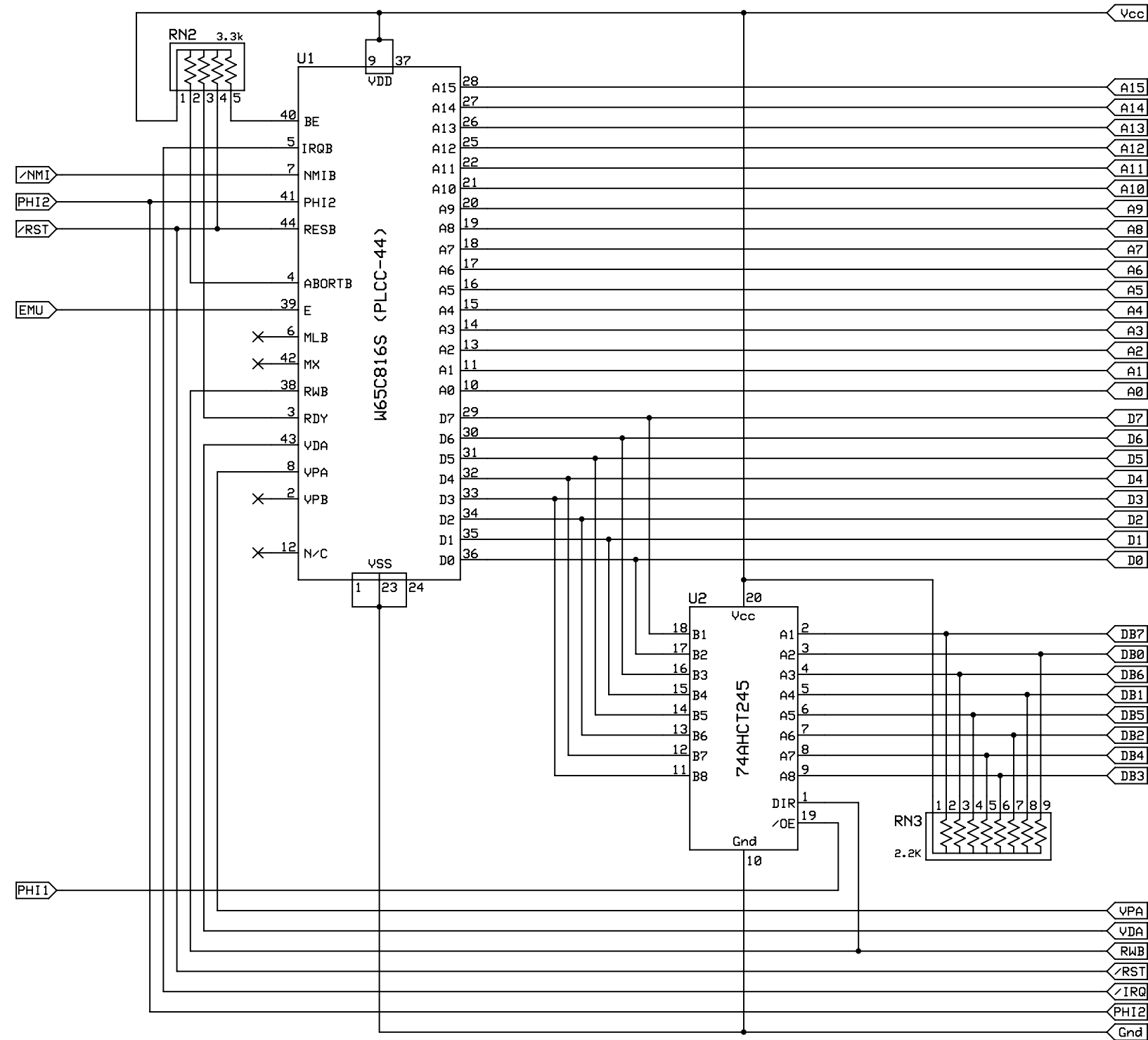


RESET, NMI, INDICATORS

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	Rev 1.5	



* C20 → Y1



Decoupling Capacitors -- 0.1 uf @ 50v

MPU INTERFACE

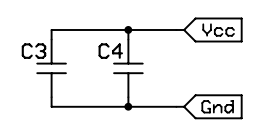
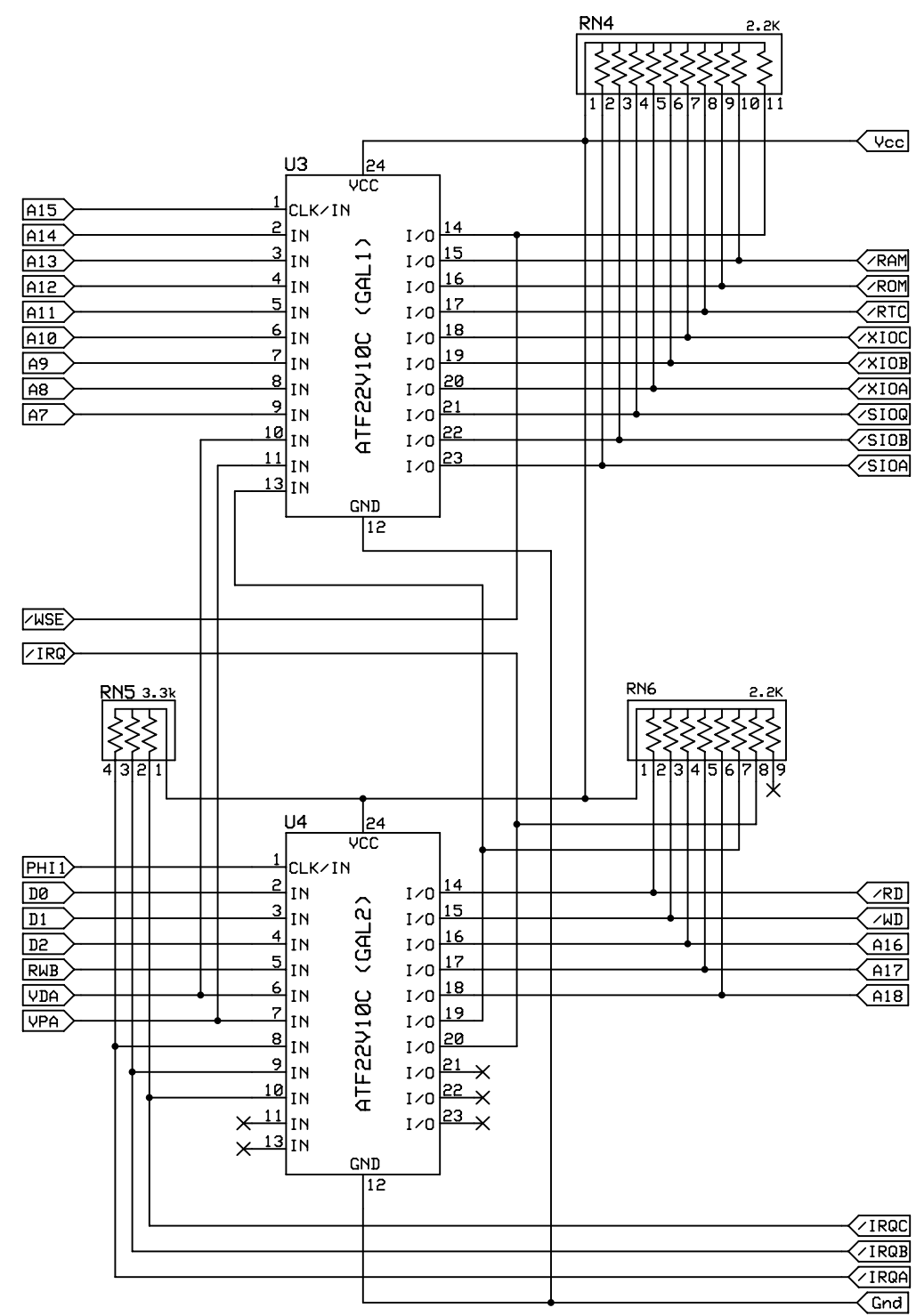
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2024/01/01
Rev 1.5

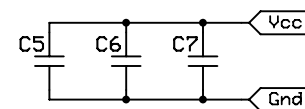
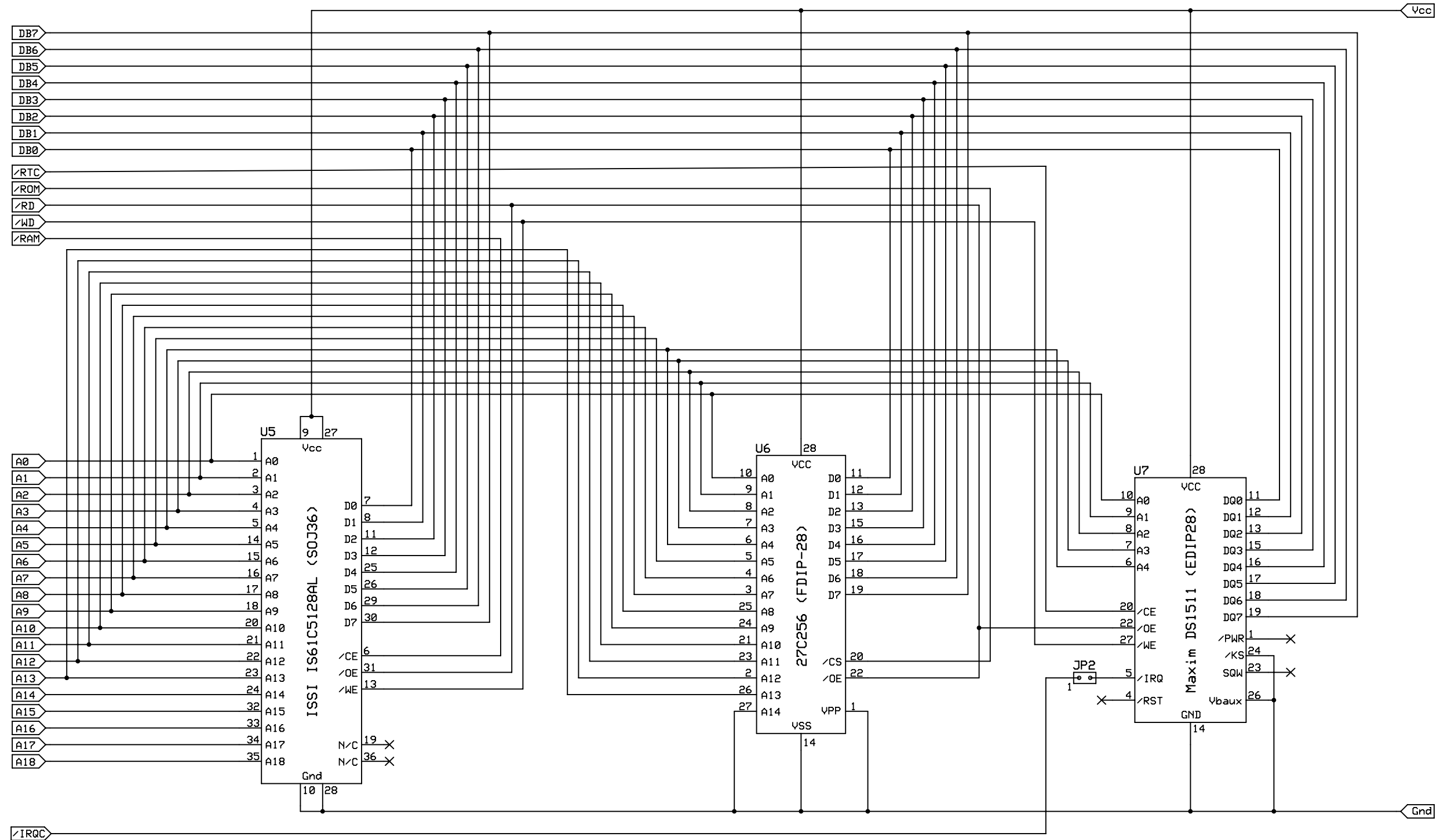
Page 5 of 10



Decoupling Capacitors -- 0.1 uf @ 50v

GLUE LOGIC

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BigDumbDinosaur	2024/01/01	Page 6 of 10
	Rev 1.5	



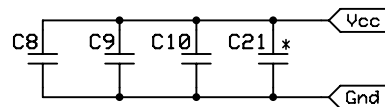
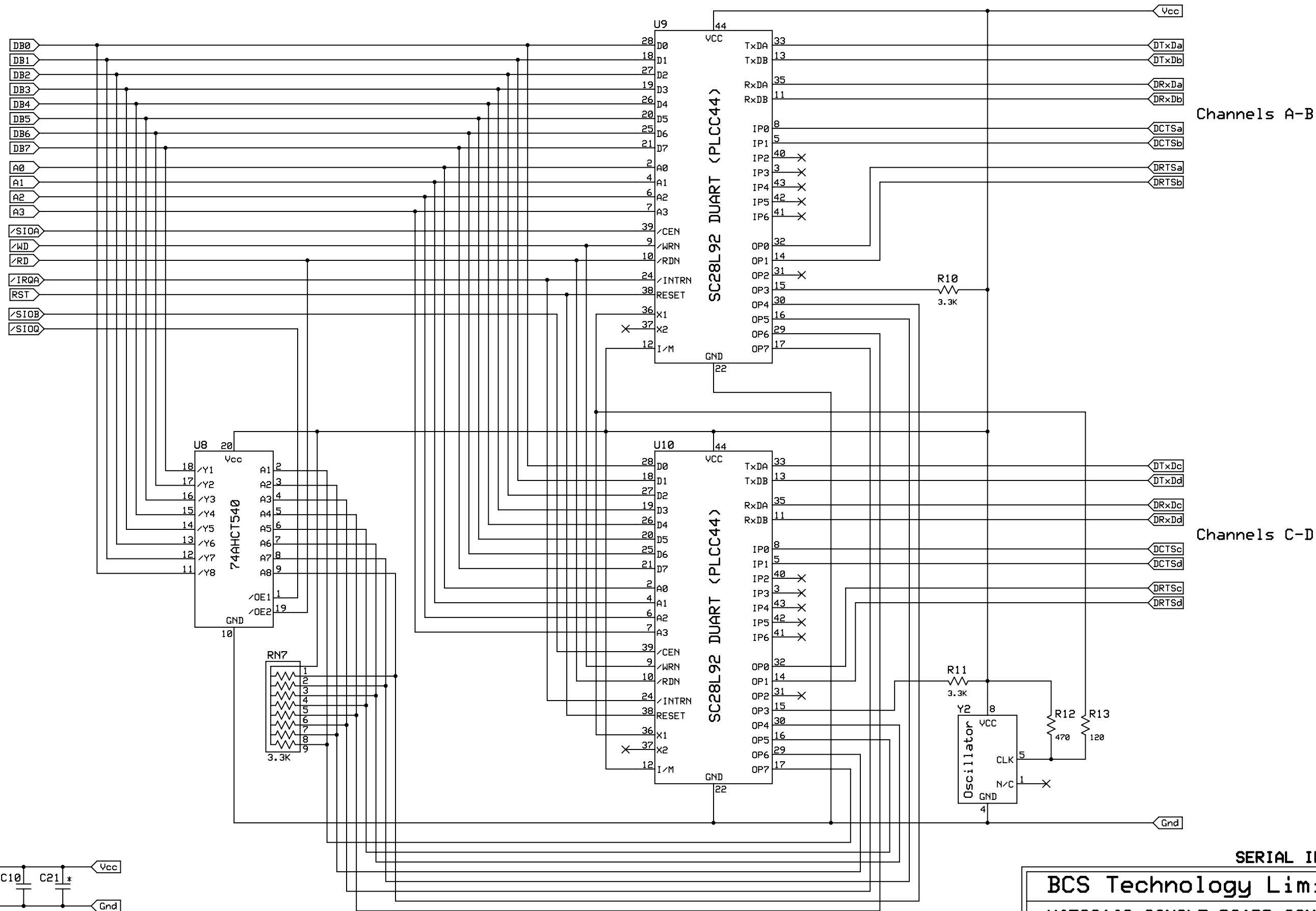
Decoupling Capacitors -- 0.1 uf @ 50v

RAM, ROM & RTC

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W65C816S SINGLE BOARD COMPUTER

BigDumbDinosaur	2024/01/01	Page 7 of 10
	Rev 1.5	



Decoupling Capacitors -- 0.1 uf @ 50v

* C21 → Y2

SERIAL INTERFACE

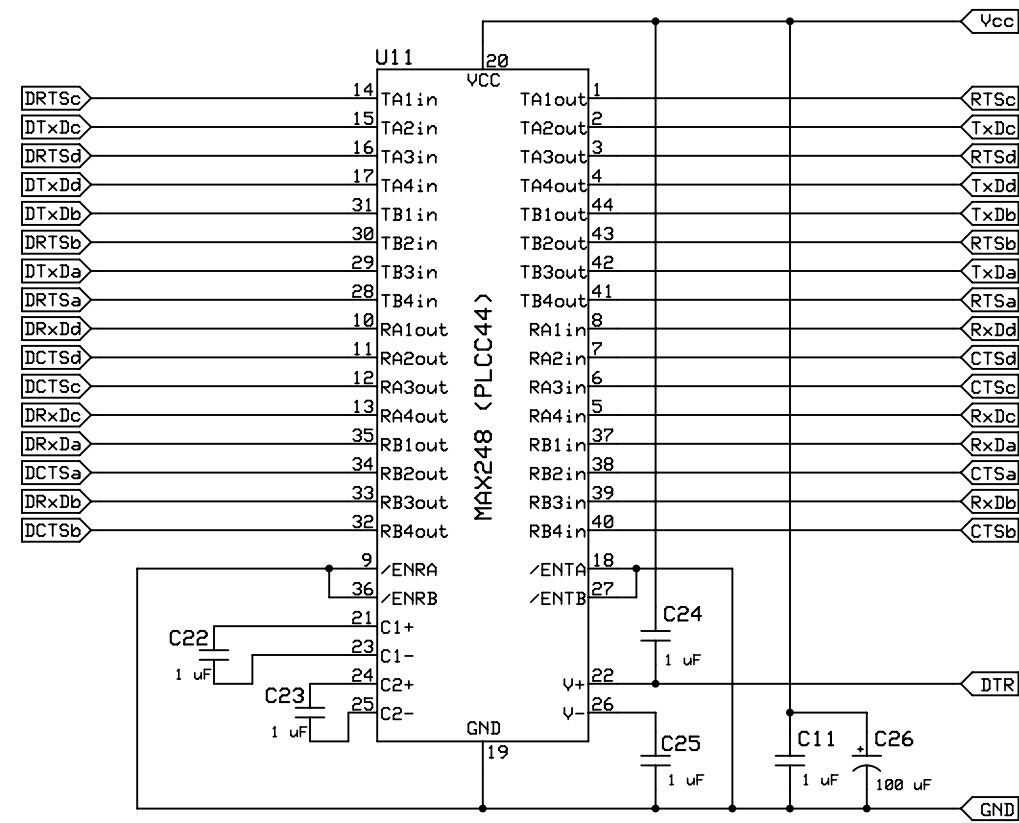
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2024/01/01
Rev 1.5

Page 8 of 10



TIA232 INPUT/OUTPUT

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2024/01/01
Rev 1.5

Page 9 of 10

