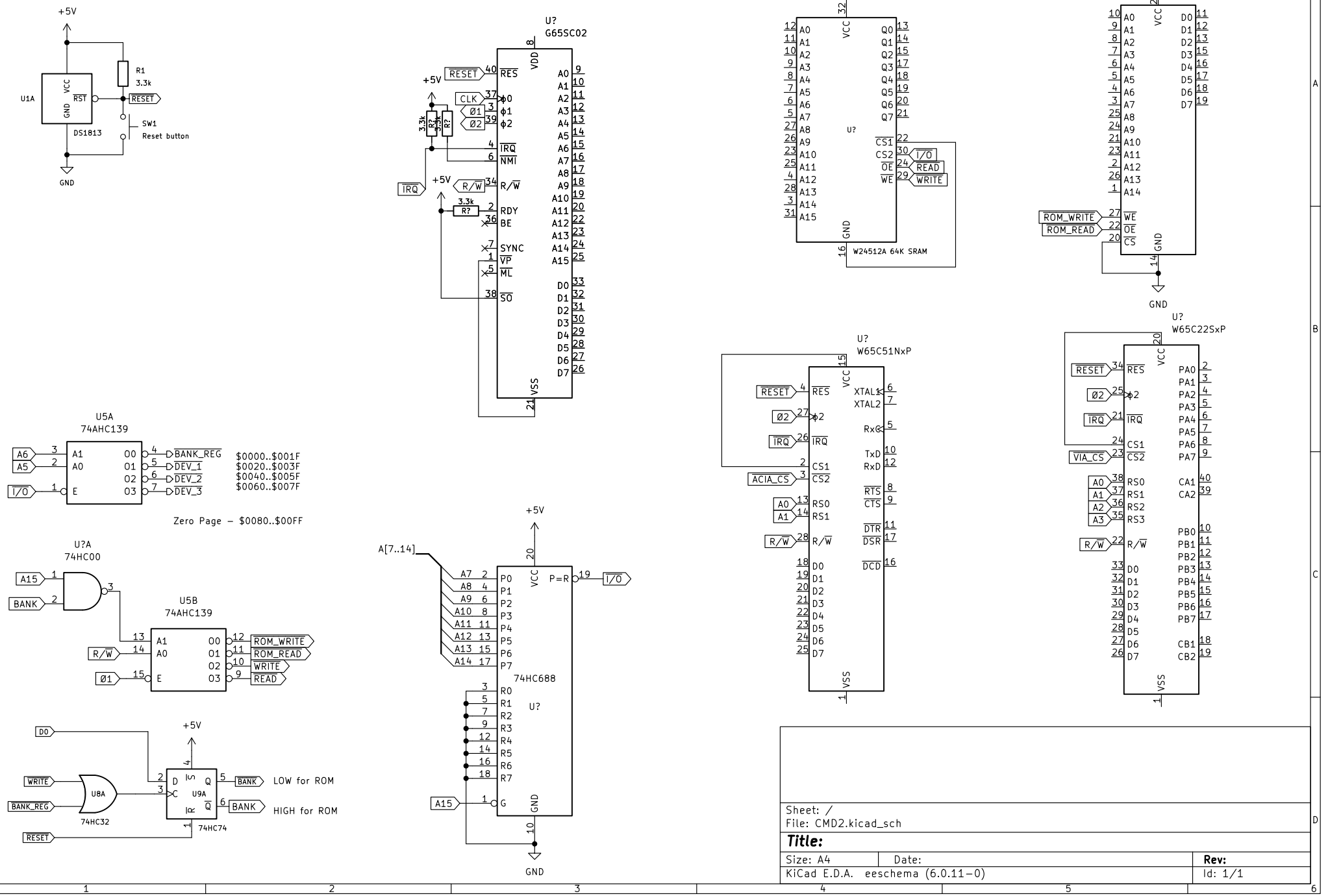
+5V
U1A
VCC
GND
RST
DS1813
R1
3.3k
RESET
SW1
Reset button
GND
U?
G65SC02
VDD
RES
Ø0
Ø1
Ø2
IRQ
NMI
R/W
RDY
BE
SYNC
VP
ML
SO
VSS
A0
A1
A2
A3
A4
A5
A6
A7
A8
A9
A10
A11
A12
A13
A14
A15
D0
D1
D2
D3
D4
D5
D6
D7
CLK
3.3k
R?
U?
W24512A 64K SRAM
CS1
CS2
OE
WE
I/O
READ
WRITE
U?
28C256
ROM_WRITE
ROM_READ
U5A
74AHC139
A6
A5
I/O
BANK_REG
DEV_1
DEV_2
DEV_3
$0000..$001F
$0020..$003F
$0040..$005F
$0060..$007F
Zero Page – $0080..$00FF
U?A
74HC00
A15
BANK
U5B
74AHC139
R/W
Ø1
ROM_WRITE
ROM_READ
WRITE
READ
U?
W65C51NxP
RESET
ACIA_CS
RS0
RS1
RTS
CTS
DTR
DSR
DCD
RxD
TxD
XTAL1
XTAL2
U?
W65C22SxP
VIA_CS
PA0
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7
CA1
CA2
CB1
CB2
A[7..14]
P=R
74HC688
U?
D0
WRITE
BANK_REG
RESET
U8A
74HC32
U9A
74HC74
BANK
LOW for ROM
HIGH for ROM
Sheet: /
File: CMD2.kicad_sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. eeschema (6.0.11-0)
Id: 1/1