Independent Bit Output

Port A

Select
J1

U1 74HC138

=> Port A

=> Port B

Data
J5

74AHC04

Data[0..7]

U2 74HC245

U3 74HC245

U8 74HC245

U9 74HC245

74HC279

Port A Output / Status
J2

Port A Input
J3

Addr	Write	Read
00	A Reset bits	A Status
01	A Set bits	A Input
10	B Reset bits	B Status
11	B Set bits	B Input

LDA #%00001100
STA IBO+0 ; Reset bits 2 and 3 without affecting others
STA IBO+1 ; Set bits 2 and 3 without affecting others
LDA IBO+0 ; Read actual status of all bits
LDA IBO+1 ; Read some 8bit input
....
Intended for CapsLk, NumLk, ReadyToRead, DataReady, HDD LED and similar on/off independent output bits

Port B (Same as Port A)

Leftovers

U7C 74AHC04
U7D 74AHC04
U7E 74AHC04
U7F 74AHC04
U4E 74HC279
U5E 74HC279
U6G 74AHC04
U7G 74AHC04

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