Address generation:
Line count * 0x30 + character count
(each line twice)

Line timing

Field timing

pixel addressable 384x300 video at 20MHz dot clock

DNB projects

Sheet: /Line timing/
File: line timing_sch.kicad_sch

Title: SVGA address and sync timing generator

Size: A3	Date: 2023-11-16	**Rev: a**
KiCad E.D.A. kicad 7.0.9		Id: 2/2