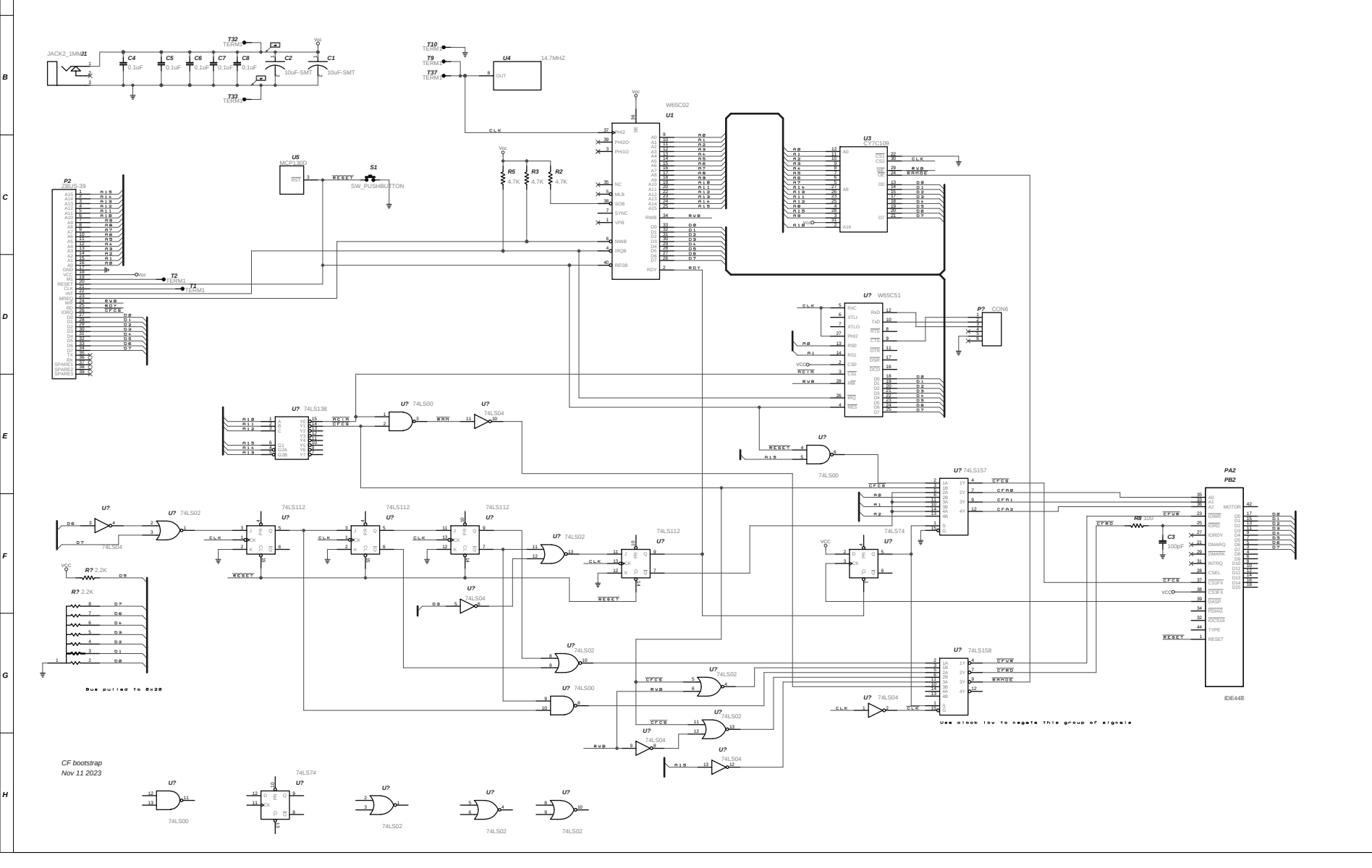
JACK2_1MM
J1
C4 0.1uF
C5 0.1uF
C6 0.1uF
C7 0.1uF
C8 0.1uF
C2 10uF-SMT
C1 10uF-SMT
T32 TERM1
T33 TERM1
Vcc
T10 TERM1
T9 TERM1
T37 TERM1
U4 14.7MHZ
OUT
CLK
W65C02
U1
PHI2
PHI2O
PHI1O
NC
MLB
SOB
SYNC
VPB
NMIB
IRQB
RESB
RDY
R5 4.7K
R3 4.7K
R2 4.7K
U5 MCP130
RST
S1 SW_PUSHBUTTON
RESET
P2 ZBUS-39
T2 TERM1
T1 TERM1
U3 CY7C109
U? W65C51
P? CON6
U? 74LS138
U? 74LS00
U? 74LS04
U? 74LS157
U? 74LS74
U? 74LS112
U? 74LS02
U? 74LS158
R? 2.2K
Bus pulled to 3v3B
R8 100
C3 100pF
PA2
PB2
IDE44B
Use clock low to negate this group of signals
CF bootstrap
Nov 11 2023