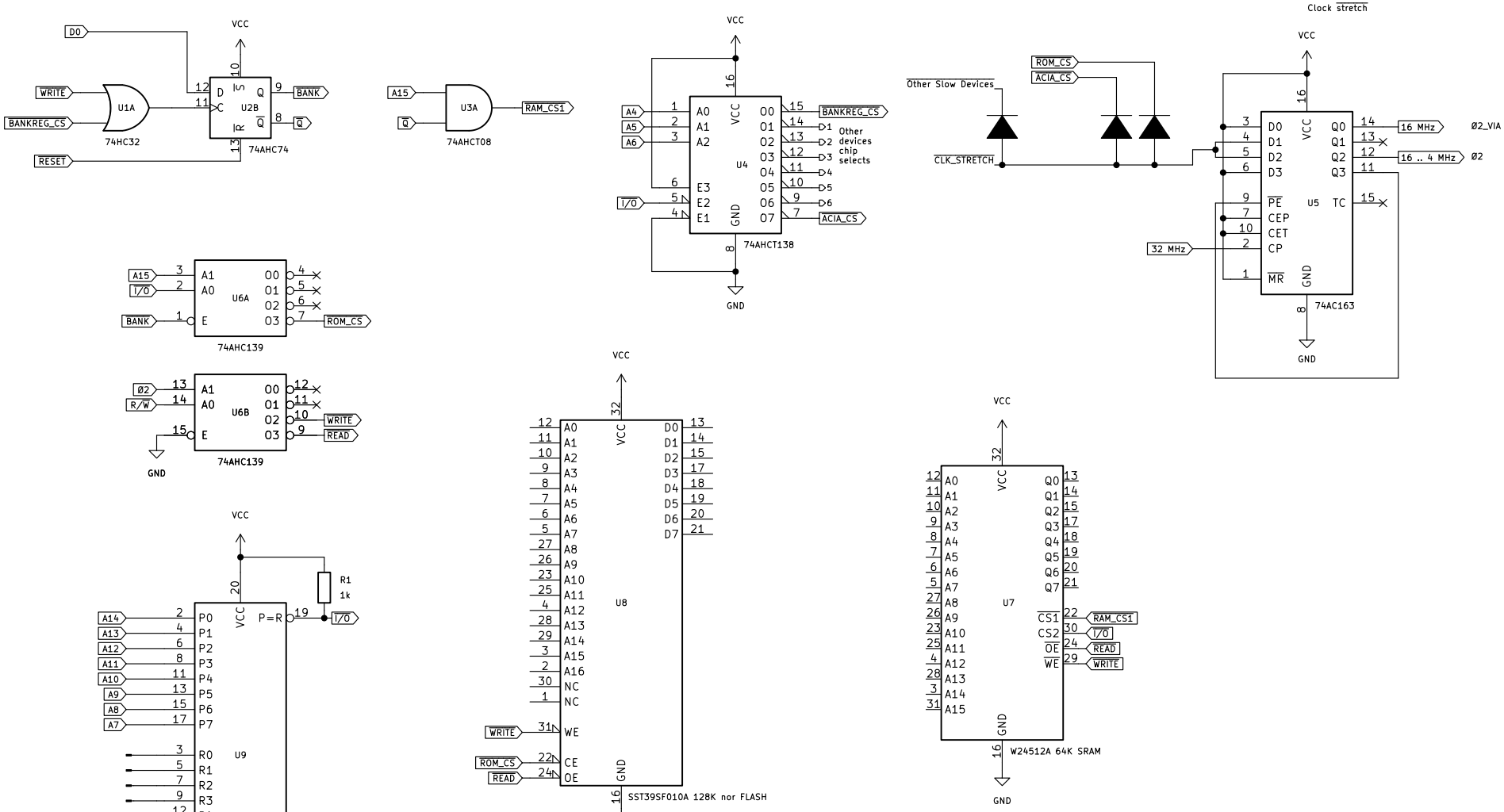
D0
VCC
WRITE
BANKREG_CS
U1A
74HC32
U2B
74AHC74
BANK
Q
RESET
A15
Q
U3A
74AHCT08
RAM_CS1
A4
A5
A6
I/O
E3
E2
E1
U4
74AHCT138
BANKREG_CS
Other devices chip selects
ACIA_CS
GND
Clock stretch
ROM_CS
ACIA_CS
Other Slow Devices
CLK_STRETCH
U5
74AC163
16 MHz
Ø2_VIA
16 .. 4 MHz
Ø2
32 MHz
A15
I/O
BANK
U6A
74AHC139
ROM_CS
Ø2
R/W
U6B
74AHC139
WRITE
READ
GND
R1
1k
P=R
A14
A13
A12
A11
A10
A9
A8
A7
U9
74ALS520
Pin header for front panel switch connector
UB
SST39SF010A 128K nor FLASH
READ
U7
W24512A 64K SRAM
Sheet: /
File: BlueAugust2.kicad_sch
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. kicad (6.0.11-0)
Id: 1/1