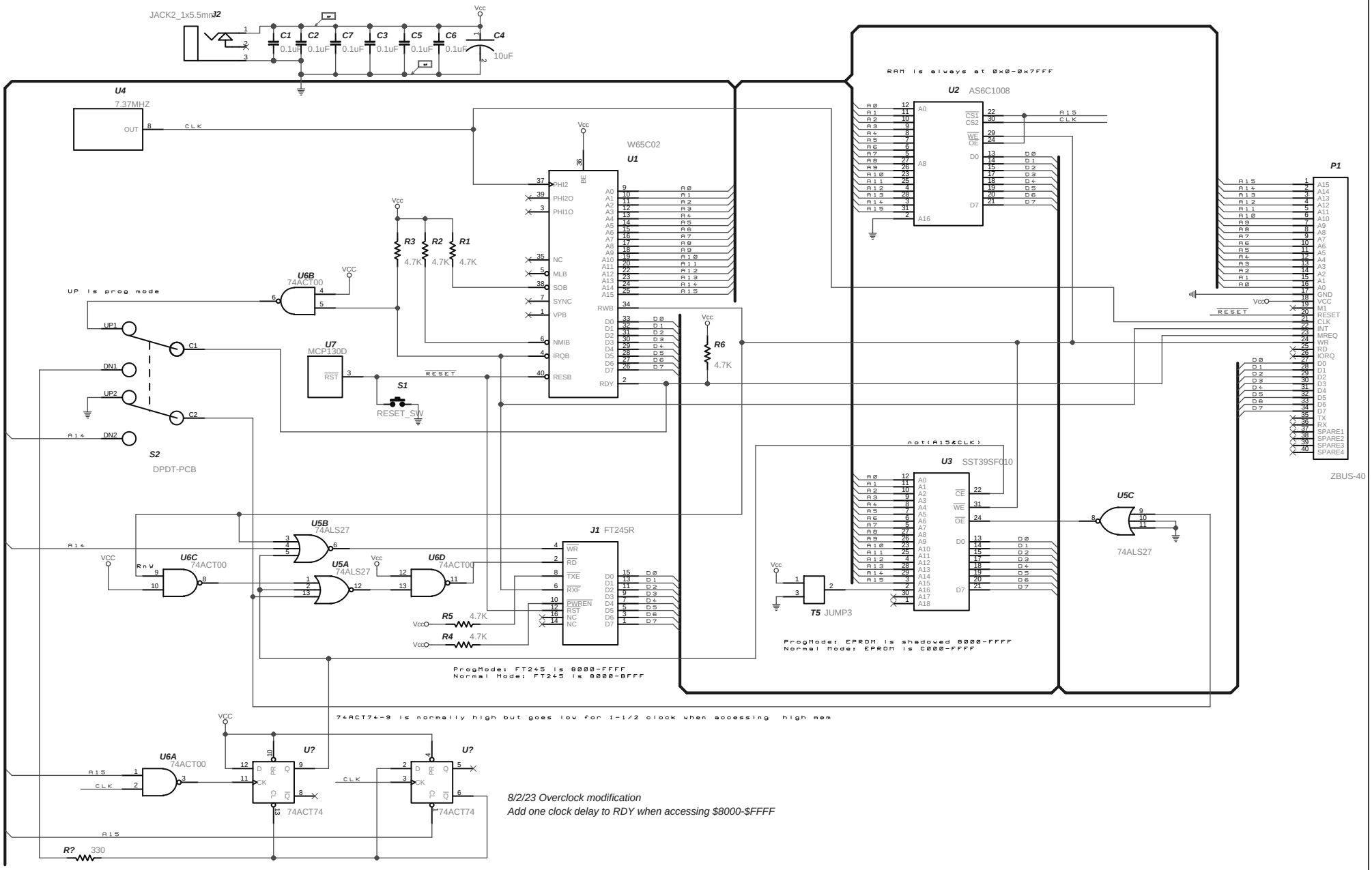

8/2/23 Overclock modification
Add one clock delay to RDY when accessing $8000-$FFFF

Memory Map in Programming mode:
Flash, shadow from 0x8000-0xFFFF
RAM, 0x0-0x7FFF
FT245R, 0x8000-0xFFFF

Memory Map in Normal mode:
RAM, 0x0-0x7FFF
FT245R, 0x8000-0xBFFF
EPROM, 0xC000-0xFFFF

6502-based Flash Programmer

Rev1A 8/2/23
OverClock Experiment