CPU DATA BUS

CPU ADDRESS BUS

RAM ADDRESS BUS RA0–RA17

RAM DATA BUS RD0–RD7

U3 W65C816S6PG-14

U5 W27C512-45

U6 W65C22N6TPG-14

U7 EPM7128SLC84-10

U2 IS61C1024-10N

U4 IS61C1024-10N

J2 VGA OUT

J3 VIA I/O

J4 USB MODULE

J5 EXPANSION

J1 JTAG

U1A U1B U1C U1D U1E U1F U1G 74HCU04

D1 EMUL

D2 1N4148

D3 MX

D4 POWER

SW1 RESET

Y1 25MHz

One bypass capacitor on each power pin of CPLD

Sheet: /	
File: 816Proto.kicad_sch	
Title: 65C816 Prototype System	
Size: A3	Date: 2022-12-21
KiCad E.D.A. kicad (6.0.9)	
Rev: 0.1	
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