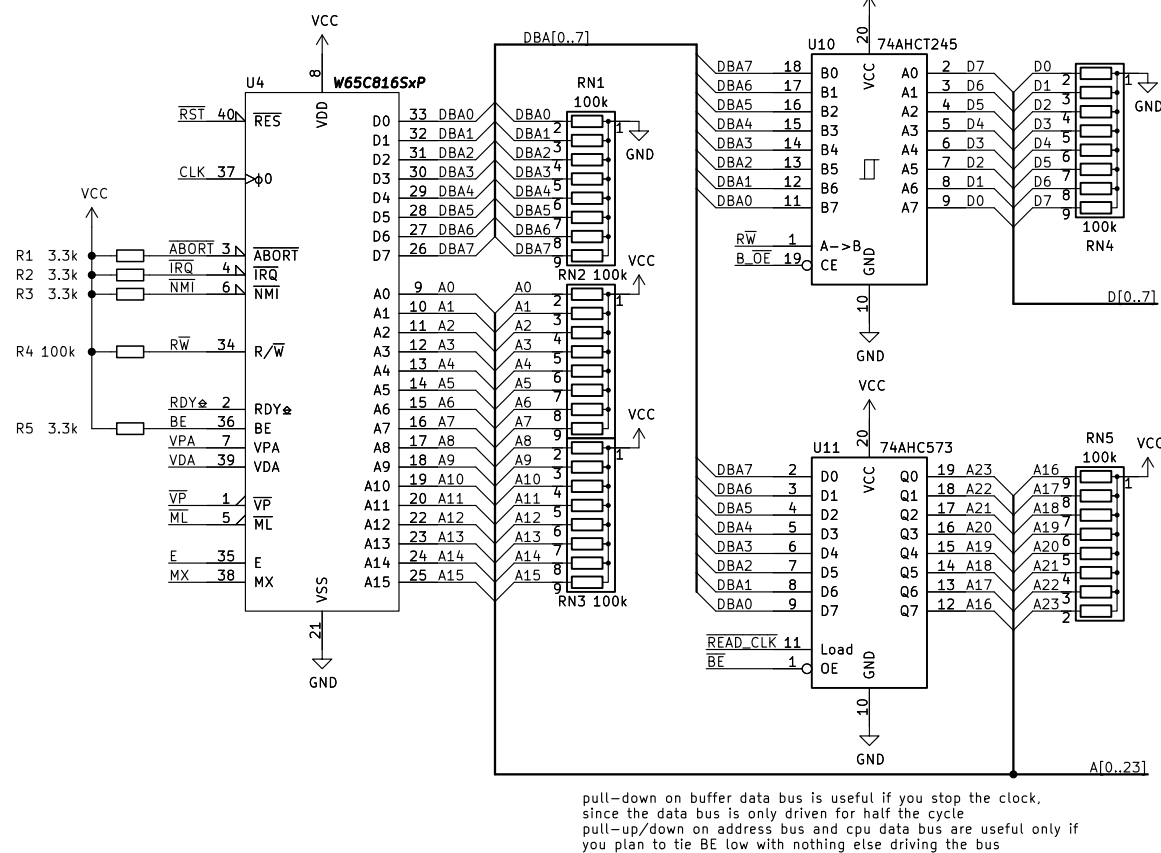
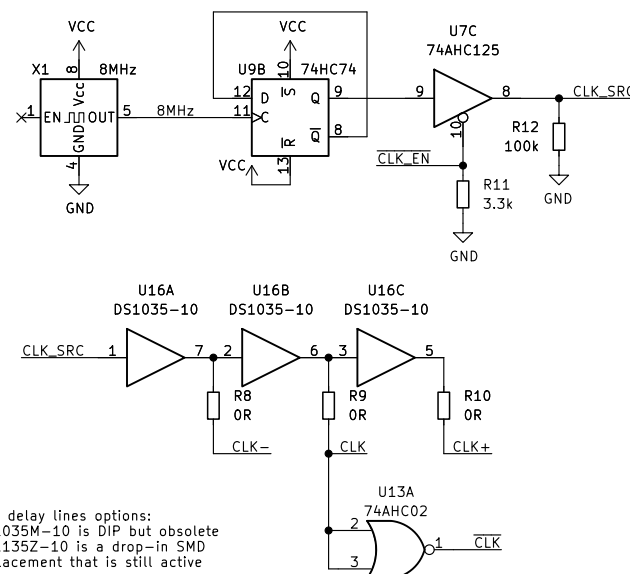


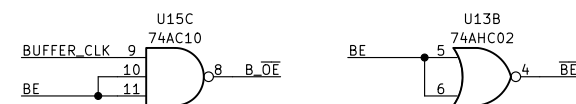
CPU & BUS MANAGEMENT



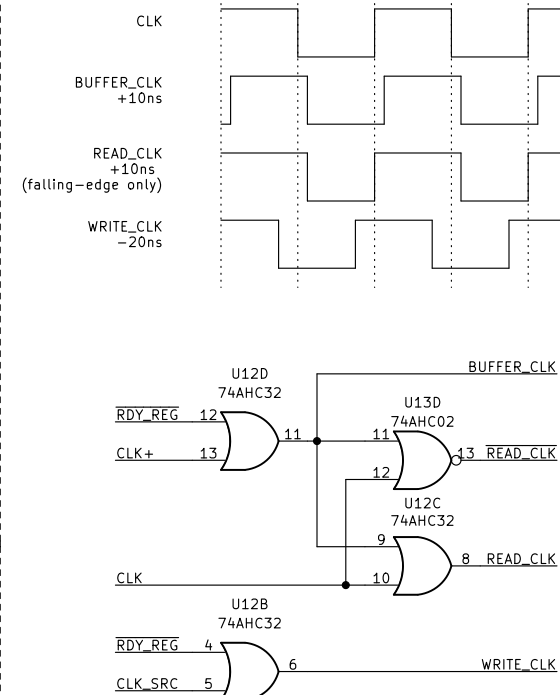
CLOCK



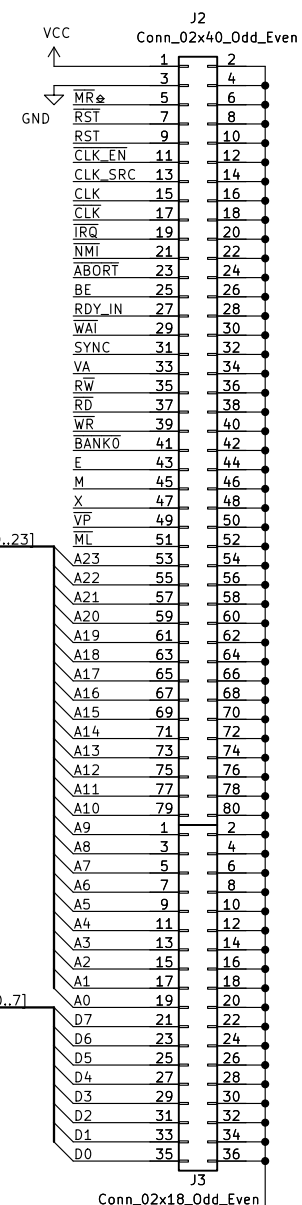
BUS ENABLE



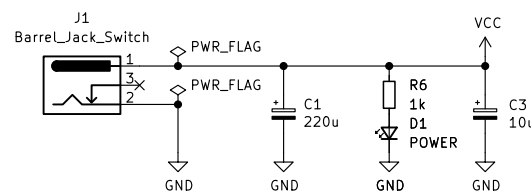
CLOCK PHASES



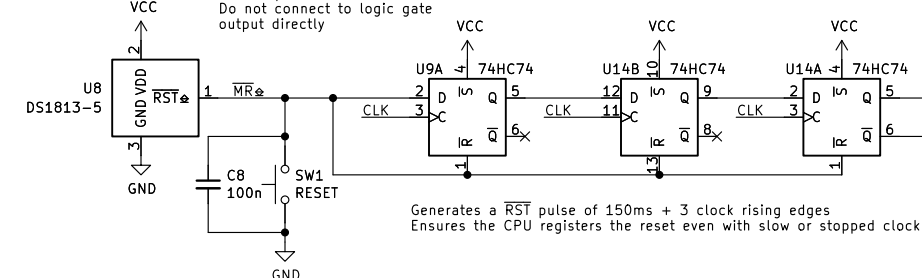
OUTPUT



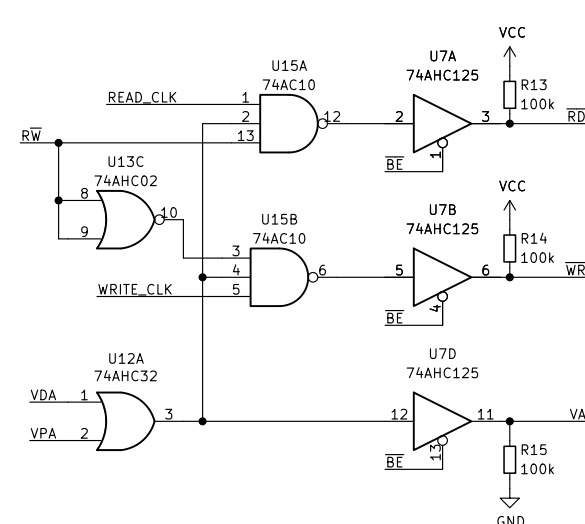
POWER



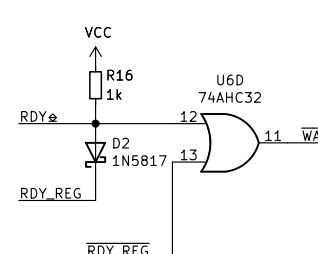
RESET



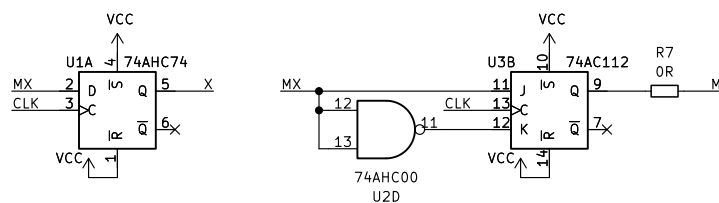
I/O CONTROL



READY PIN

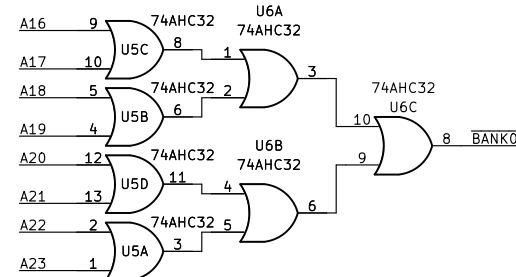


M/X DEMUX

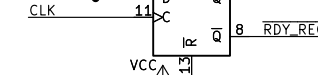


Note: 74'112 triggers on the falling edge of the clock, whereas 74'74 triggers on the rising edge

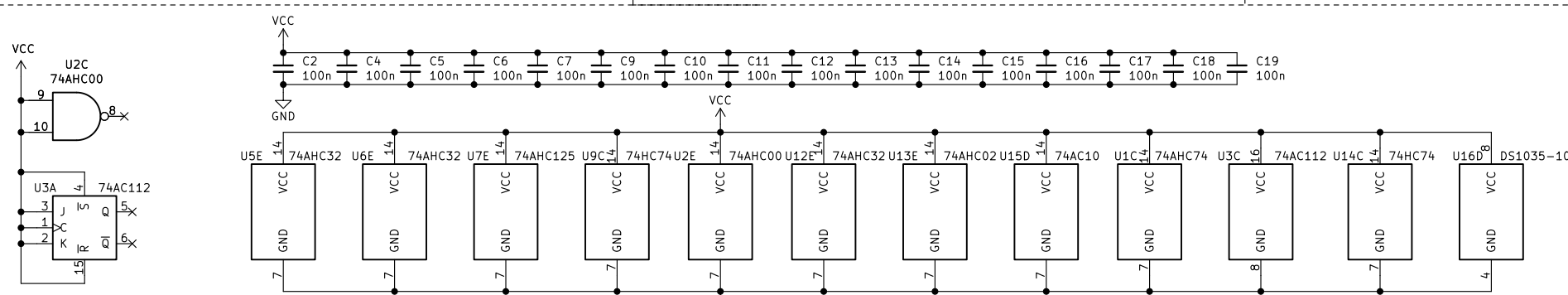
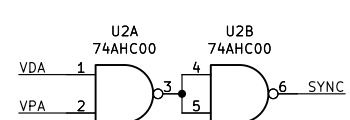
BANK ZERO



TEST POINTS



SYNC



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<https://github.com/adrienkohlbecker/65C816/tree/main/hardware/breakout>

Sheet: /
File: bre

Title: 65C816 Breakout Board

Size: A3	Date: 2022-08-24
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KiCad E.D.A. kicad (6.0.7-1)-1

Rev: A.27

Id: 1/1