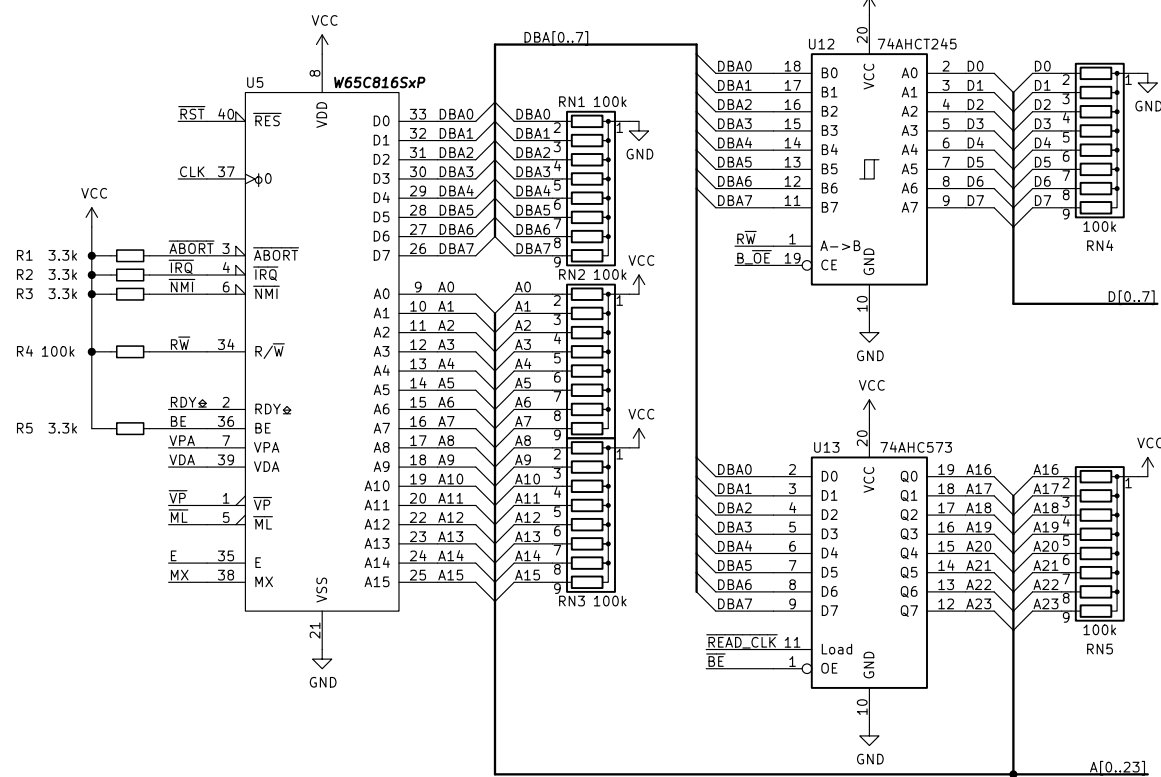
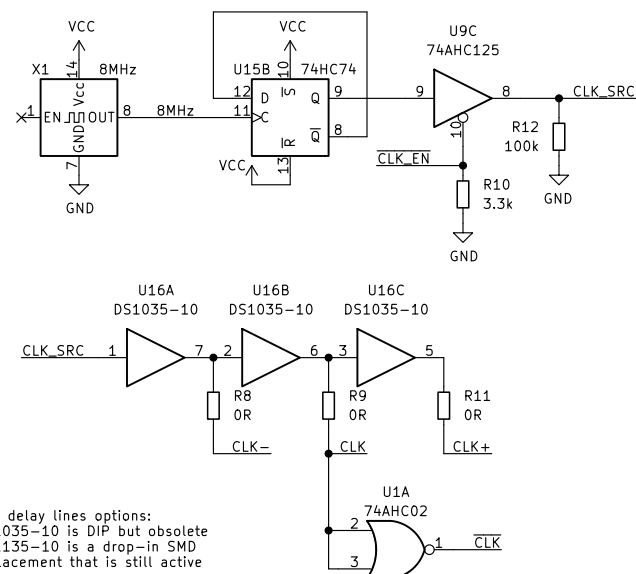


CPU & BUS MANAGEMENT

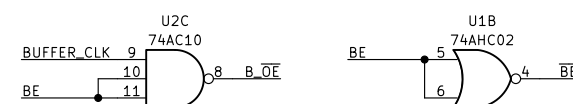


pull-down on buffer data bus is useful if you stop the clock,
since the data bus is only driven for half the cycle
pull-up/down on address bus and cpu data bus are useful only if
you plan to tie BE low with nothing else driving the bus

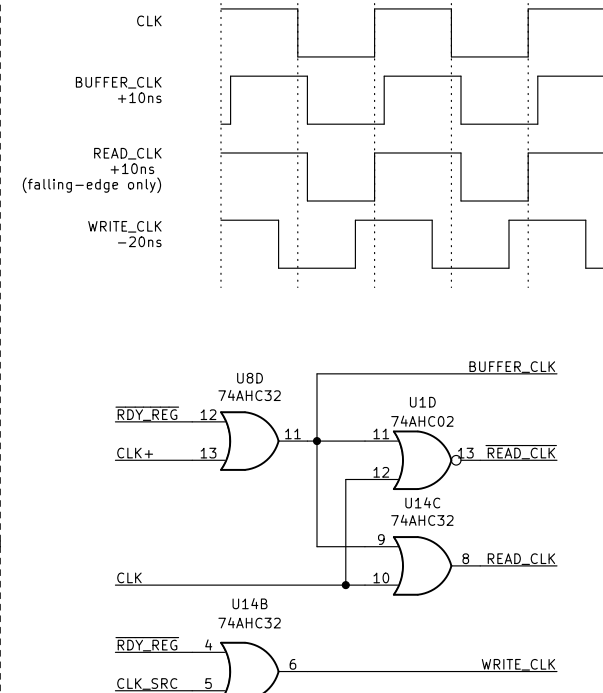
CLOCK



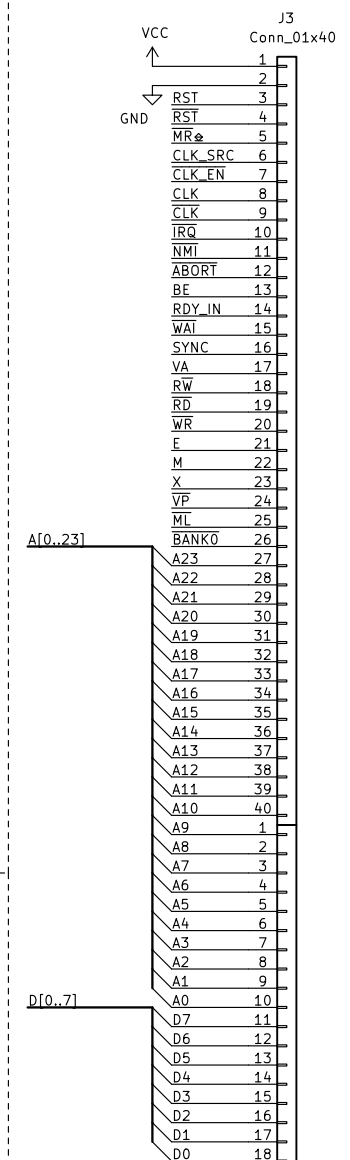
BUS ENABLE



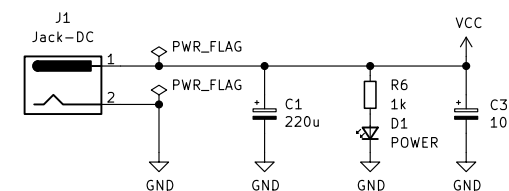
CLOCK PHASES



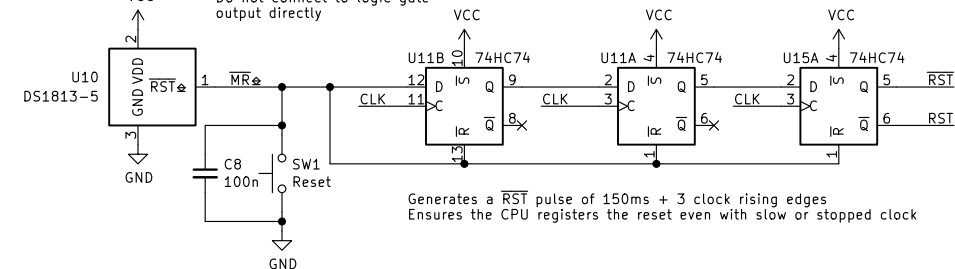
OUTPUT



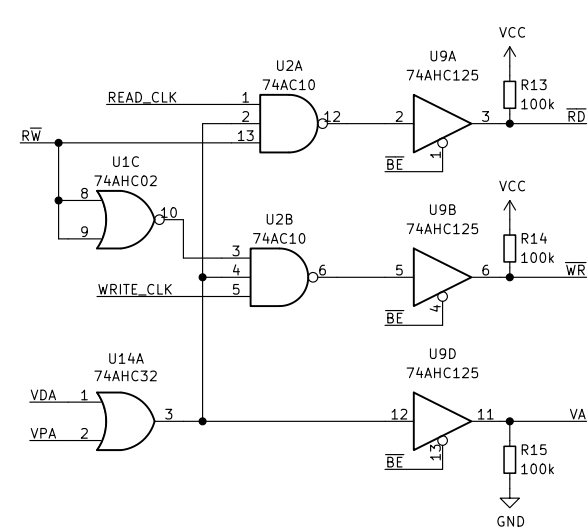
POWER



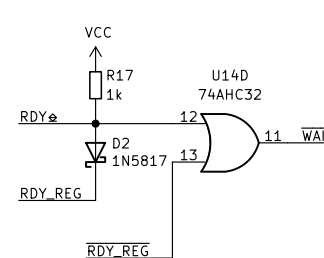
RESET



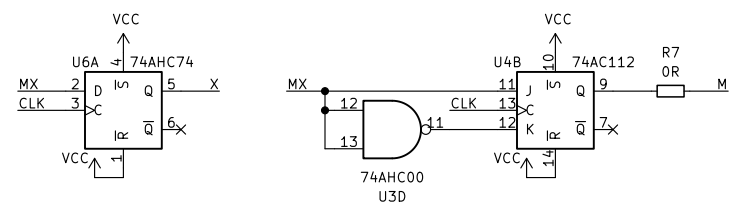
I/O CONTROL



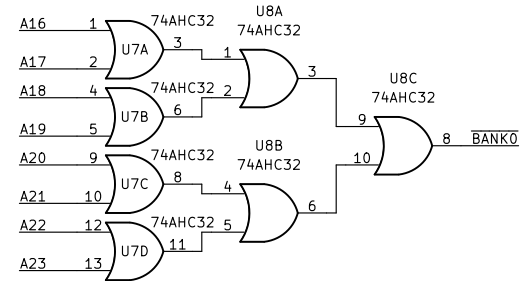
READY PIN



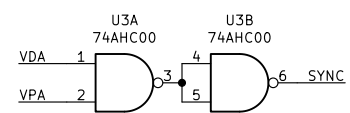
M/X DEMUX



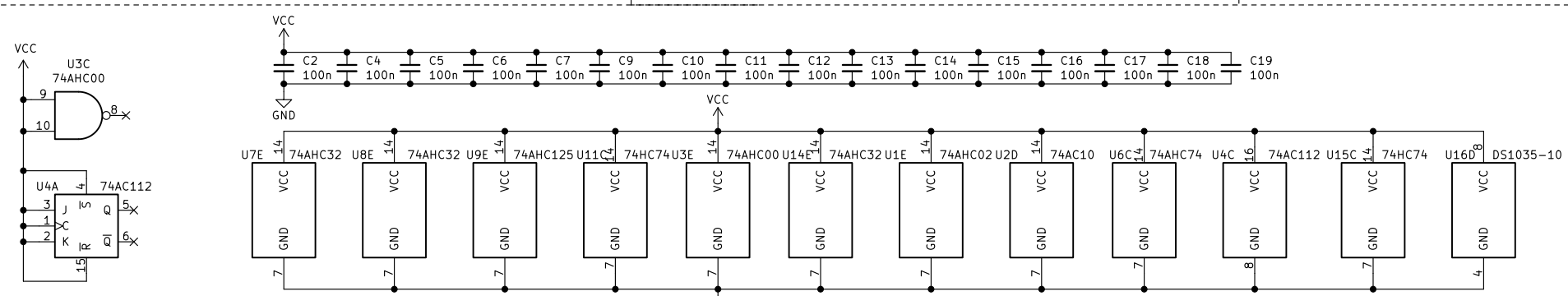
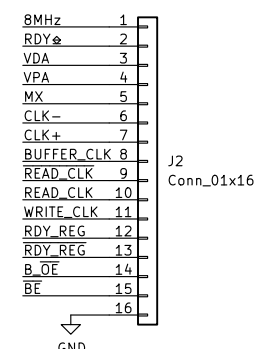
BANK ZERO



SYNC



TEST POINTS



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<https://github.com/adrienkohlbecker/65C816/tree/main/hardware/breakout>

Sheet: /
File: breakout.kicad_sch

Title: 65C816 Breakout Board

Size: A3	Date: 2022-07-28
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Source: KiCad E.D.A.	Version: kicad (6.0.6-1)-1
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Rev: A.25

Id: 1/1