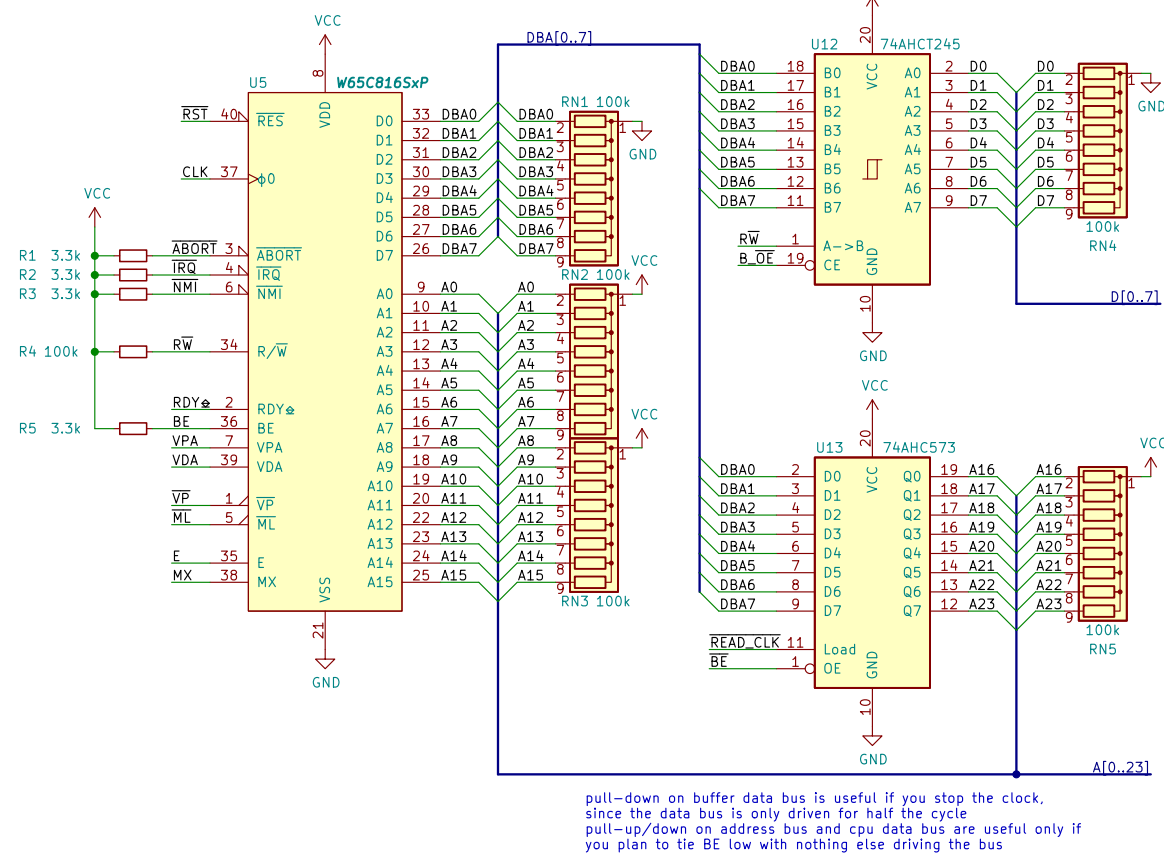
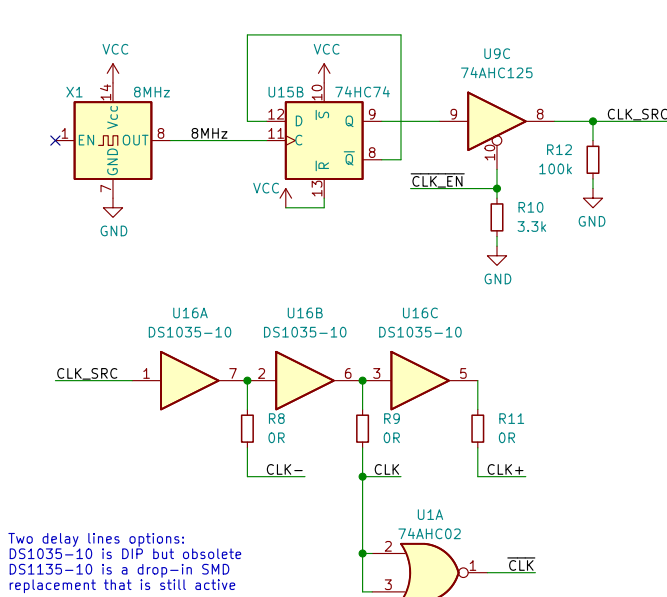


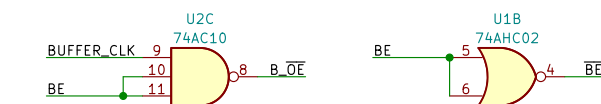
CPU & BUS MANAGEMENT



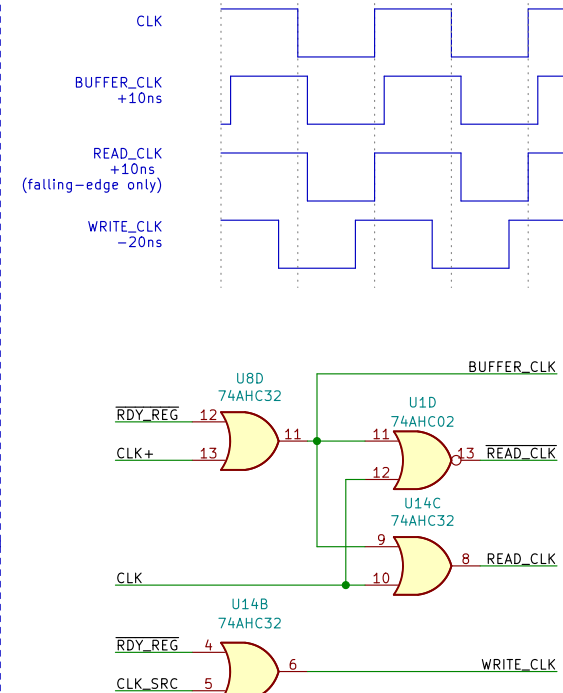
CLOCK



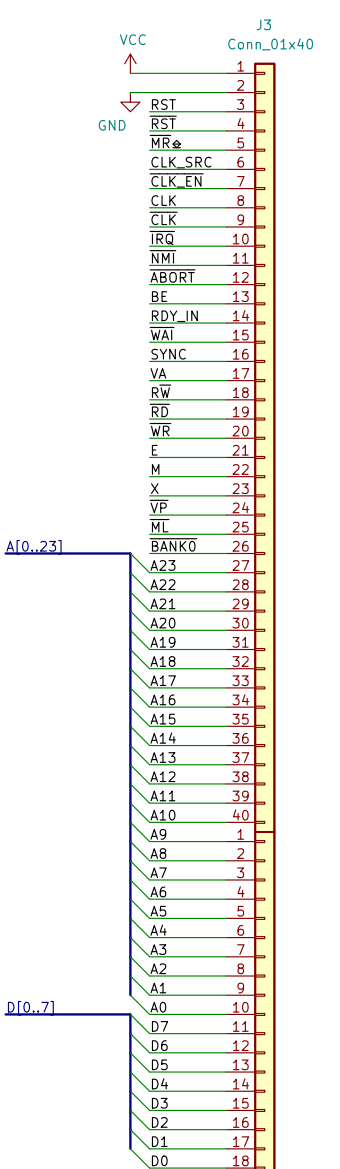
BUS ENABLE



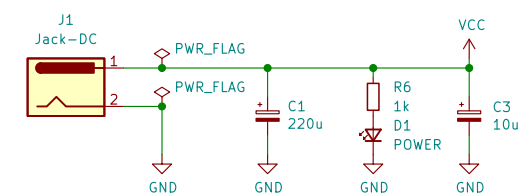
CLOCK PHASES



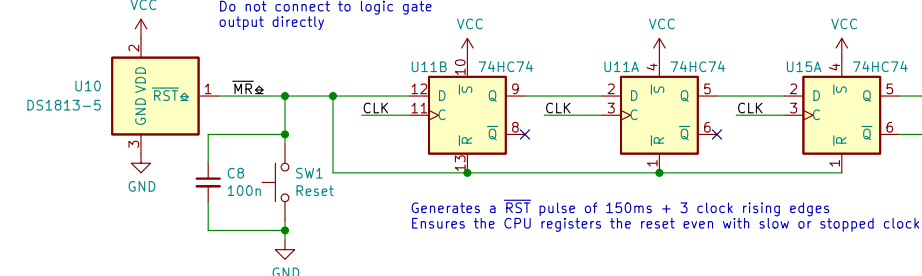
OUTPUT



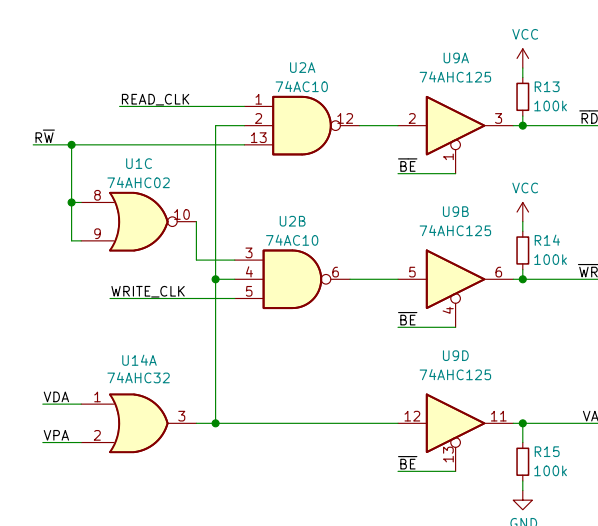
POWER



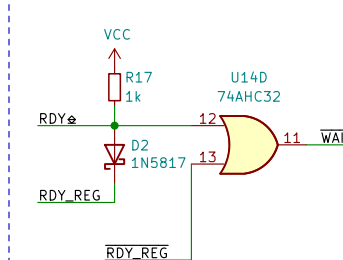
RESET



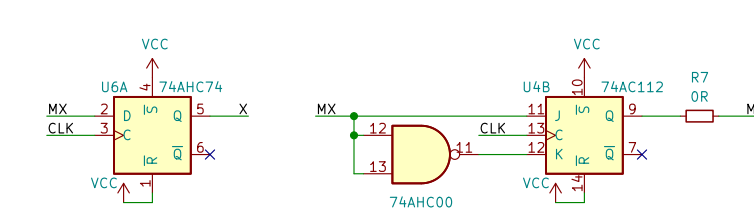
I/O CONTROL



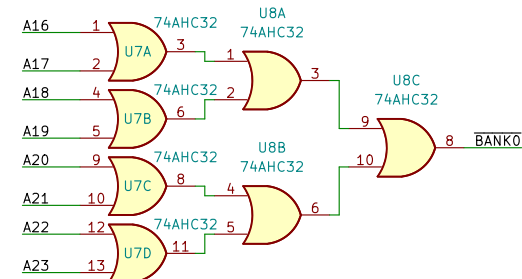
READY PIN



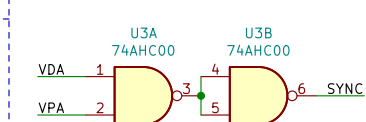
M/X DEMUX



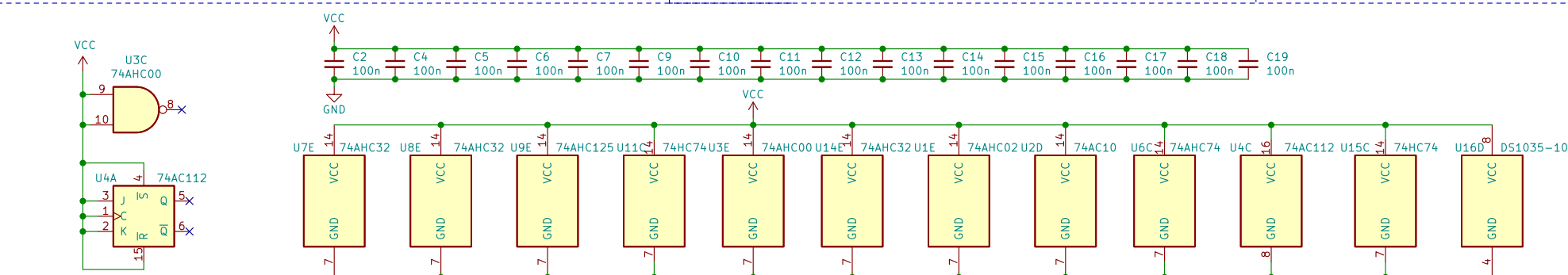
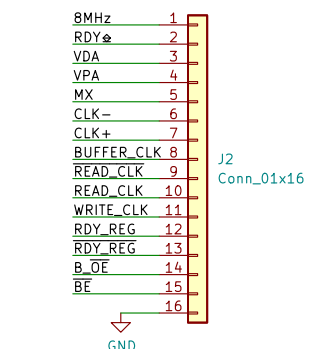
BANK ZERO



SYNC



TEST POINTS



Copyright © 2022 Adrien Kohlbecker
Licensed under CERN-OHL-W v2
<https://github.com/adrienkohlbecker/65C816/tree/main/hardware/breakout>

Sheet: /

Sheet: /
File: breakout.kicad_sch

Title: 65C816 Breakout Board

Size: A3	Date: 2022-07-28
----------	------------------

Size: A5	Date: 2022-0
KiCad E.D.A.	kicad (6.0.6-1)-1

Rev: A.25

Id: 1/1