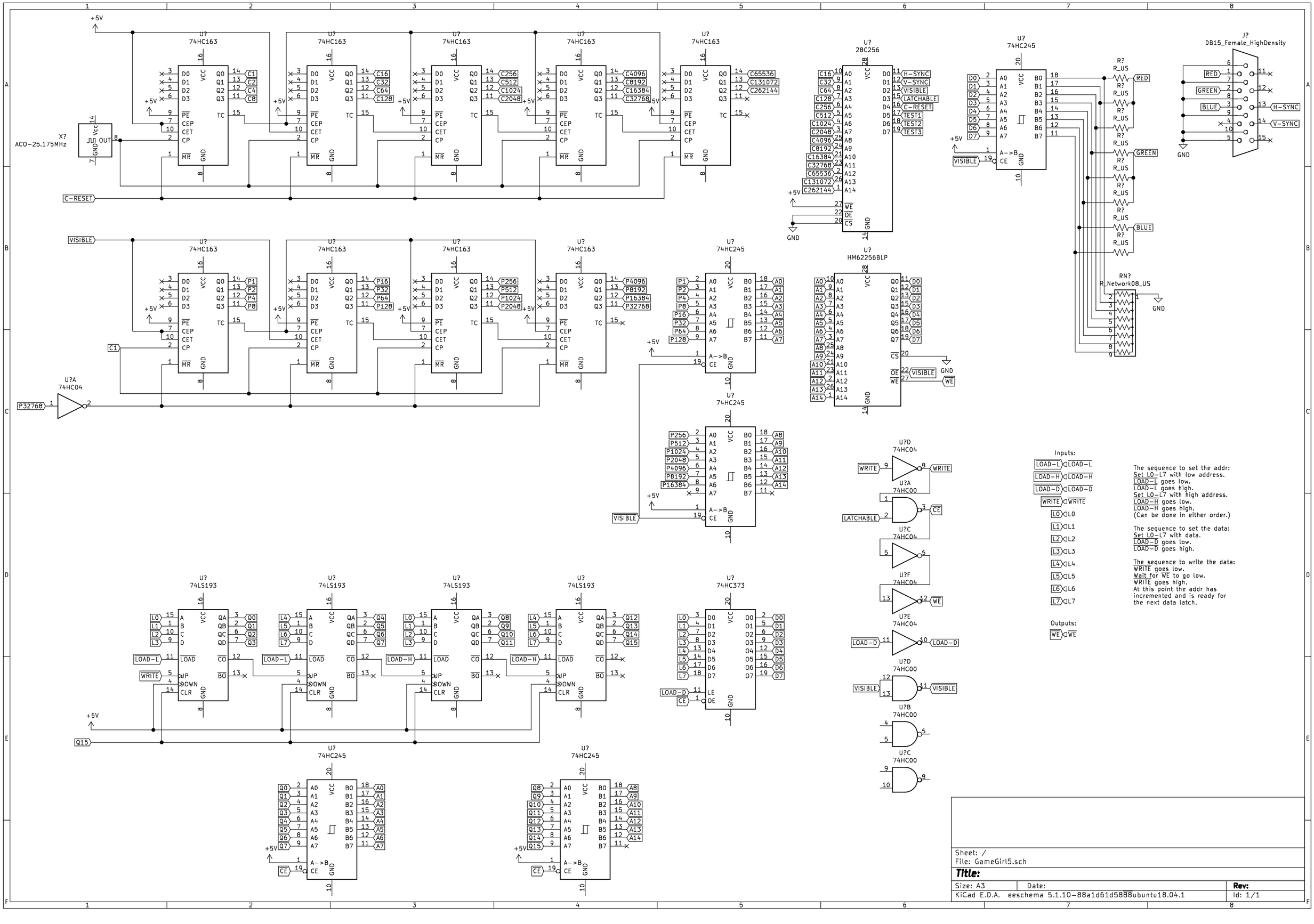
U? 74HC163
X? ACO-25.175MHz
C-RESET
U? 28C256
U? 74HC245
J? DB15_Female_HighDensity
R? R_US
RN? R_Network08_US
VISIBLE
C1
P32768
U?A 74HC04
U? 74HC245
U? HM62256BLP
U?D 74HC04
U?A 74HC00
U?C 74HC04
U?F 74HC04
U?E 74HC04
U?D 74HC00
U?B 74HC00
U?C 74HC00
U? 74LS193
U? 74HC373
U? 74HC245
Inputs:
Outputs:
The sequence to set the addr:
Set L0–L7 with low address.
LOAD–L goes low.
LOAD–L goes high.
Set L0–L7 with high address.
LOAD–H goes low.
LOAD–H goes high.
(Can be done in either order.)
The sequence to set the data:
Set L0–L7 with data.
LOAD–D goes low.
LOAD–D goes high.
The sequence to write the data:
WRITE goes low.
Wait for WE to go low.
WRITE goes high.
At this point the addr has incremented and is ready for the next data latch.
Sheet: /
File: GameGirl5.sch
Title:
Size: A3
Date:
Rev:
KiCad E.D.A. eeschema 5.1.10–88a1d61d5888ubuntu18.04.1
Id: 1/1