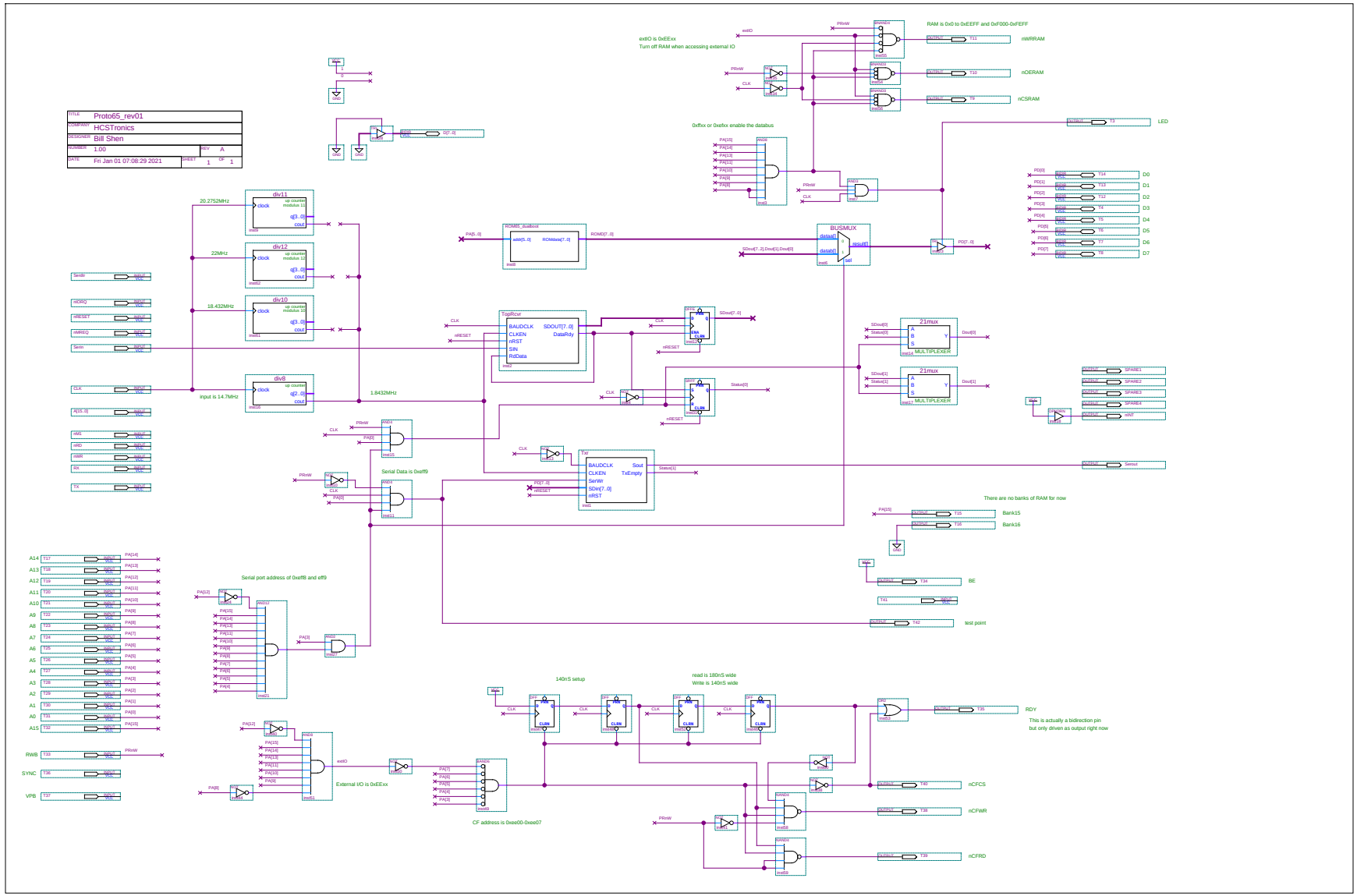
TITLE Proto65_rev01
COMPANY HCSTronics
DESIGNER Bill Shen
NUMBER 1.00
REV A
DATE Fri Jan 01 07:08:29 2021
SHEET 1 OF 1
extIO is 0xEExx
Turn off RAM when accessing external IO
RAM is 0x0 to 0xEEFF and 0xF000-0xFEFF
nWRRAM
nOERAM
nCSRAM
LED
0xffxx or 0xefxx enable the databus
div11
div12
div10
div8
20.2752MHz
22MHz
18.432MHz
input is 14.7MHz
1.8432MHz
ROM65_dualbank
BUSMUX
TopRcvr
BAUDCLK
CLKEN
nRST
SIn
RxData
SOut[7..0]
DataRdy
21mux
MULTIPLEXER
Serial Data is 0xeff9
Serial port address of 0xeff8 and eff9
Txr
SOut
TxEmpty
SDin[7..0]
There are no banks of RAM for now
Bank15
Bank16
BE
test point
140nS setup
read is 180nS wide
Write is 140nS wide
RDY
This is actually a bidirection pin
but only driven as output right now
External I/O is 0xEExx
CF address is 0xee00-0xee07
nCFCS
nCFWR
nCFRD