

1	2	3	4	5	
A					
Sheet: CPU & Clock					
File: CPU.sch					
Sheet: Input/Output					
File: InpOutp.sch					
Sheet: Memory – RAM/ROM					
File: Memory.sch					
B					
C					
D					
1	2	3	4	5	

NOTE: Commodore used some very odd schematic/board revision references and it's hard to determine which is which. This redraw is shown as sub-version "C" but is referenced elsewhere as the "Revision N" schematic.

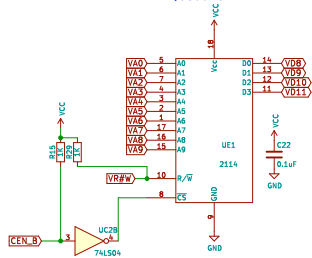
ECO 820040 12/27/1982
Transcribed from CBM VIC-20cr schematics 251027-01C
Steve J. Gray and Rich Cini

Sheet: /
File: VIC20Reloaded-1.0-001.sch

Title: VIC RELOADED

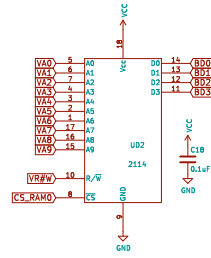
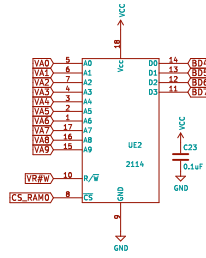
Size: A	Date: 2020-12-21	Rev: 1.0-001
KiCad E.D.A. kicad (5.1.8-0-10_14)	Id: 1/4	

COLOR RAM \$9600-97FF

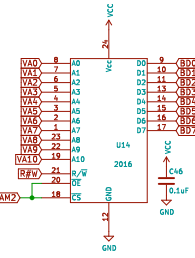
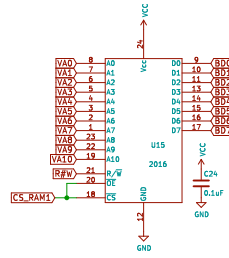


RAM - \$0000 - \$03FF

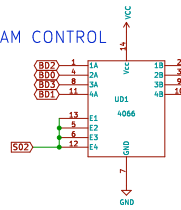
Note: On the prototype, the 2114 data pins are drawn backwards.
D0->D3 is 11->14



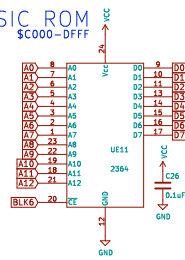
RAM - \$1000 - \$1FFF



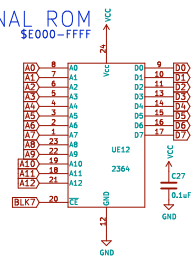
COLOR RAM CONTROL



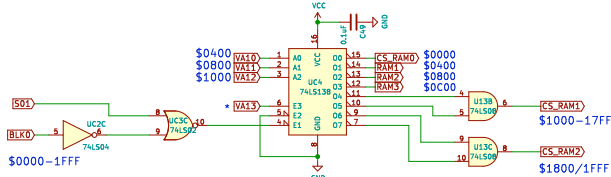
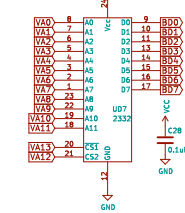
BASIC ROM \$C000-DFFF



KERNAL ROM \$E000-FFFF

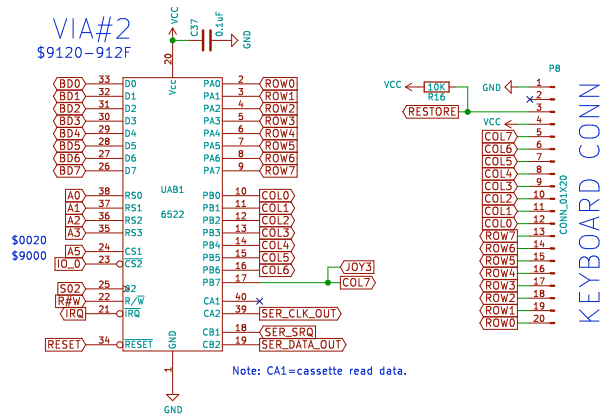


CHR ROM \$8000-BFFF

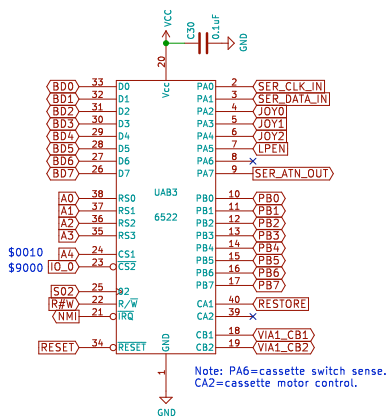


* When CPU has access VA13 is substituted with BLK4 line.

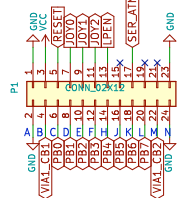
VIA#2 \$9120-912F



VIA#1 \$9110-911F



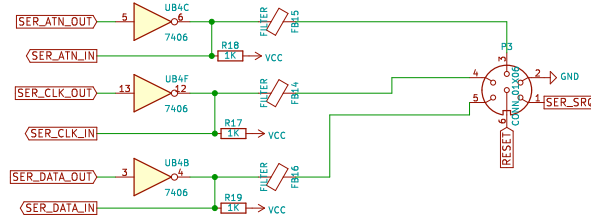
USER PORT



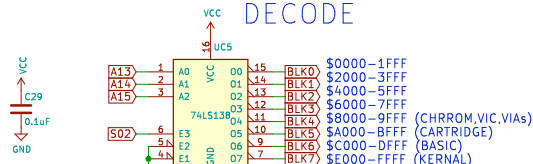
Note: pin 8 was connected to the cassette sense switch (not used here). Pins 10/11 were connected to 9VAC from the power brick.

TOP is numbered 1 -> 12
BOTTOM is lettered A -> N

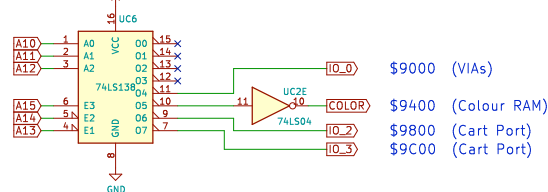
IEC PORT



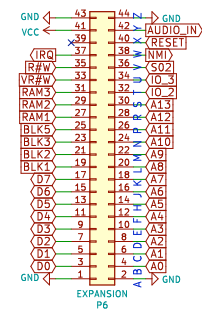
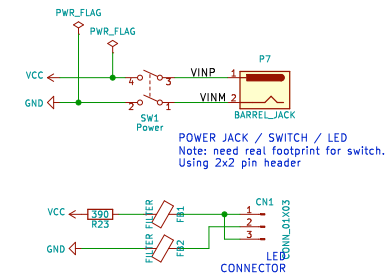
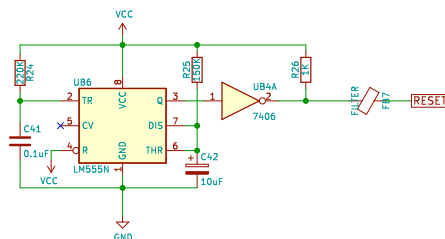
DECODE



100 000 > \$8000 > 0
100 001 > \$8400 > 1
100 010 > \$8800 > 2
100 011 > \$8C00 > 3
100 100 > \$9000 > 4
100 101 > \$9400 > 5
100 110 > \$9800 > 6
100 111 > \$9C00 > 7



RESET



Cartridge is about 6" wide with the plastic or 5.25" without.

Note: On the prototype, pin Y is listed as NC.
Edac 337-044-520-202
Sullins EBM22DRXH

